TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSVI)

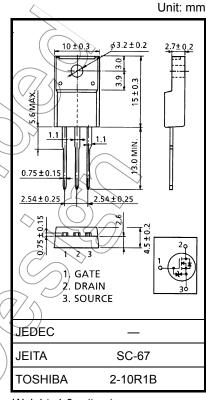
2SK4106

Switching Regulator Applications

- Low drain-source ON resistance: $R_{DS (ON)} = 0.4 \Omega (typ.)$
- High forward transfer admittance: |Y_{fs}| = 8.5 S (typ.)
- Low leakage current: $I_{DSS} = 100 \mu A \text{ (max) (V}_{DS} = 500 \text{ V)}$
- Enhancement mode: V_{th} = 2.0 to 4.0 V (V_{DS} = 10 V, I_D = 1 mA)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	500	$(\langle v \rangle)$	
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)		V_{DGR}	500	*	
Gate-source voltage		V_{GSS}	±30	Ň	
Drain current	DC (Note 1)	ΙD	12	\supset	
	Pulse (t = 1 ms) (Note 1)	I _{DP}	48	Α	
Drain power dissipati	on (Tc = 25°C)	P _D <	45	W	
Single pulse avalanche energy (Note 2)		EAS	364	mJ	
Avalanche current		I _{AR})) 12	Α	
Repetitive avalanche energy (Note 3)		EAR	4	mJ	
Channel temperature		Teh	150	7,c	
Storage temperature	range	√T _{stg}	-55 to 150)°C	
		/ //		_	



Weight: 1.9 g (typ.)

Note: Using continuously under neavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	Rth (ch-c)	2.78	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	62.5	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: $V_{DD} = 90 \text{ V}$, $T_{ch} = 25^{\circ}\text{C}(\text{initial})$, L = 4.3 mH, $I_{AR} = 12 \text{ A}$, $R_G = 25 \Omega$

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

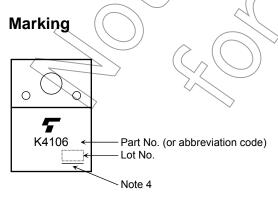
This transistor is an electrostatic-sensitive device. Please handle with caution.

Electrical Characteristics (Ta = 25°C)

Chara	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I _{GSS}	$V_{GS} = \pm 25 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μΑ
Gate-source brea	ıkdown voltage	V (BR) GSS	$I_G = \pm 10 \ \mu A, \ V_{DS} = 0 \ V$	±30	_		V
Drain cut-off curre	ent	I _{DSS}	V _{DS} = 500 V, V _{GS} = 0 V	_	_	100	μΑ
Drain-source brea	akdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	500	_		V
Gate threshold vo	oltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.0) /~	4.0	V
Drain-source ON	resistance	R _{DS} (ON)	V _{GS} = 10 V, I _D = 6 A	>_	0.4	0.52	Ω
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 6 A	3.5	8.5		S
Input capacitance	•	C _{iss}		_	1500		
Reverse transfer	capacitance	C _{rss}	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz	_	15		pF
Output capacitance		Coss		_	180		
Switching time	Rise time	t _r	10 V ID = 6 A VOUT VGS	-	22		
	Turn-on time	t _{on}	50 Ω	_((50) —	
	Fall time	t _f	V _{DD} ≈ 200 V	7	> 36		ns
	Turn-off time	t _{off}	Duty-≤ 1%, t _W = 10 μs		170		
Total gate charge	;	Qg) —	42	_	
Gate-source char	ge	Q _{gs}	$V_{DD} \simeq 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 12 \text{ A}$	_	23	_	nC
Gate-drain charge Q _{gd}		Qgd		_	19	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1))) I _{DR}		_	_	12	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	48	Α
Forward voltage (diode)	VDSF	$I_{DR} = 12 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	-1.7	V
Reverse recovery time	t _{rr}	I _{DR} = 12 A, V _{GS} = 0 V,	_	1200	_	ns
Reverse recovery charge	Q _{rr}	dl _{DR} /dt = 100 A/μs		16		μС

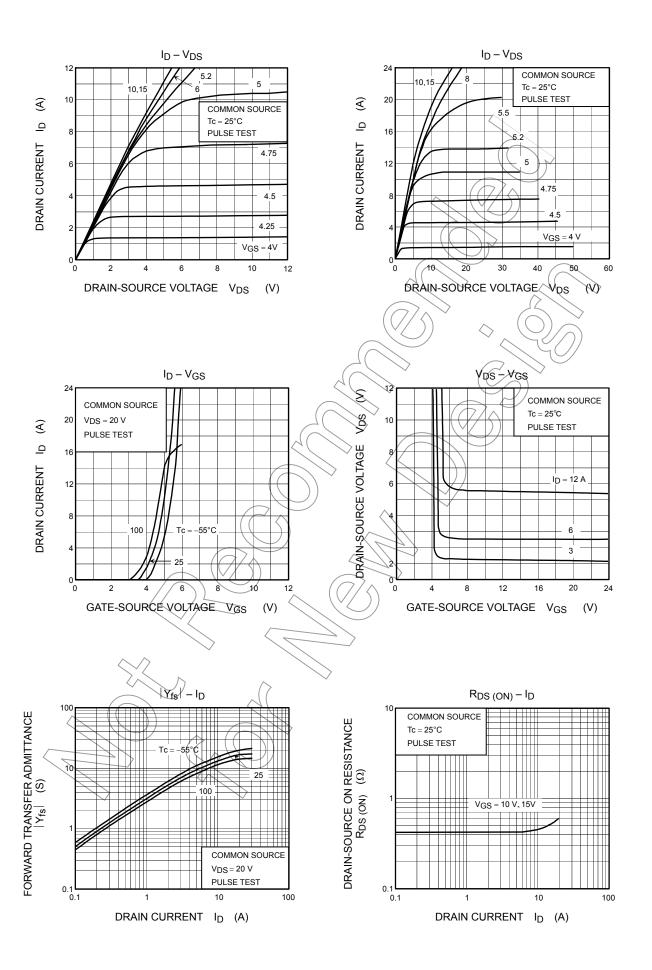


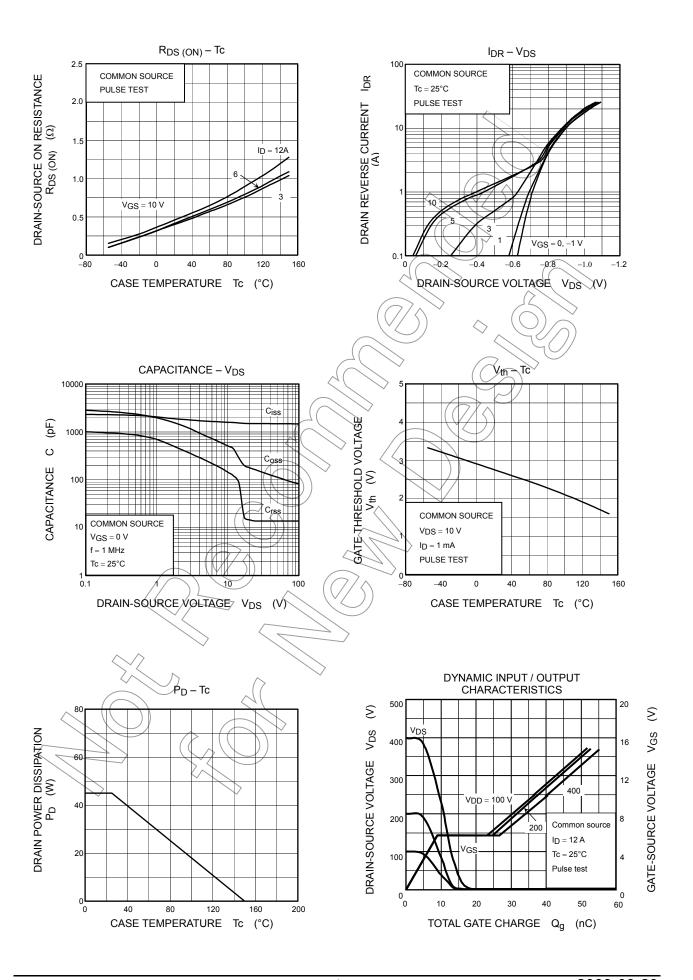
Note 4: A line under a Lot No. identifies the indication of product Labels.

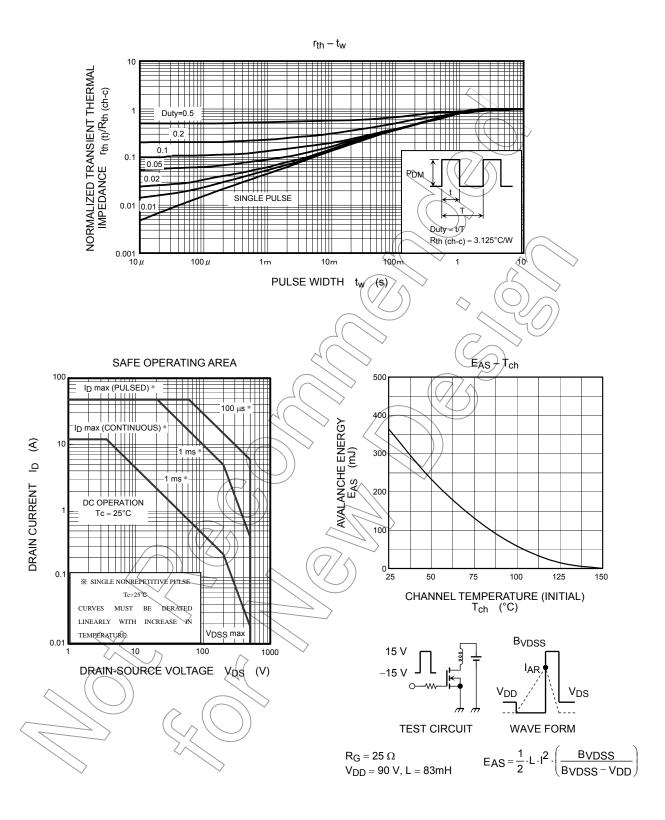
Not underlined: [[Pb]]/INCLUDES > MCV

Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

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