

HD74LVC14

Hex Schmitt-trigger Inverters

REJ03D0345-0300Z (Previous ADE-205-064B (Z)) Rev.3.00 Jul. 22, 2004

Description

The HD74LVC14 has six schmitt trigger inverters in a 14 pin package. Low voltage and high-speed operation is suitable at the battery drive product (note type personal computer) and low power consumption extends the life of a battery for long time operation.

Features

- $V_{CC} = 2.0 \text{ V to } 5.5 \text{ V}$
- All inputs V_{IH} (Max.) = 5.5 V (@ V_{CC} = 0 V to 5.5 V)
- Typical V_{OL} ground bounce < 0.8 V (@ V_{CC} = 3.3 V, Ta = 25°C)
- Typical V_{OH} undershoot > 2.0 V (@ V_{CC} = 3.3 V, Ta = 25°C)
- High output current ± 24 mA (@V_{CC} = 3.0 V to 5.5 V)
- Ordering Information

Part Name	Package Type	Package Code	Package Abbreviation	Taping Abbreviation (Quantity)		
HD74LVC14FPEL	SOP-14 pin (JEITA)	FP-14DAV	FP	EL (2,000 pcs/reel)		
HD74LVC14TELL	TSSOP-14 pin	TTP-14DV	Т	ELL (2,000 pcs/reel)		

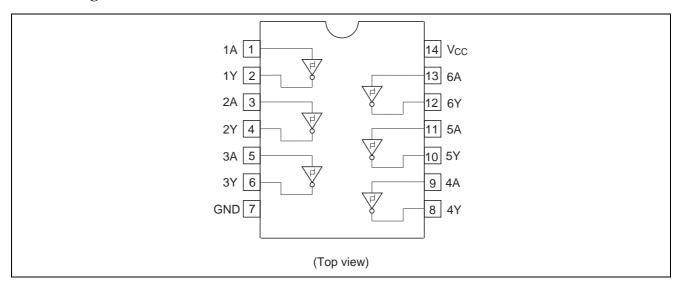
Note: Please consult the sales office for the above package availability.

Function Table

Input A	Output Y
L	Н
Н	L

H: High level L: Low level

Pin Arrangement



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Conditions
Supply voltage range	Vcc	-0.5 to 6.0	V	
Input diode current	I _{IK}	-50	mA	$V_{I} = -0.5 \text{ V}$
Input voltage	VI	-0.5 to 6.0	V	
Output diode current	I _{OK}	-50	mA	$V_{O} = -0.5 \text{ V}$
		50	_	$V_O = V_{CC} + 0.5 \text{ V}$
Output voltage	Vo	-0.5 to V _{CC} +0.5	V	
Output current	Io	±50	mA	
V _{CC} , GND current / pin	I _{CC} or I _{GND}	100	mA	
Storage temperature	Tstg	-65 to +150	°C	

Note: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

Recommended Operating Conditions

Item	Symbol	Ratings	Unit	Conditions
Supply voltage	Vcc	1.5 to 5.5	V	Data retention
		2.0 to 5.5		At operation
Input / Output voltage	Vı	0 to 5.5	V	A
	Vo	0 to V _{CC}		Υ
Operating temperature	Та	-40 to 85	°C	
Output current	I _{ОН}	-12	mA	V _{CC} = 2.7 V
		-24 ^{*2}		V _{CC} = 3.0 V to 5.5 V
	I _{OL}	12	mA	V _{CC} = 2.7 V
		24 ^{*2}		V _{CC} = 3.0 V to 5.5 V
Input rise / fall time*1	t _r , t _f	10	ns/V	

Notes: 1. This item guarantees maximum limit when one input switches.

Waveform: Refer to test circuit of switching characteristics.

2. Duty cycle ≤ 50%

Electrical Characteristics

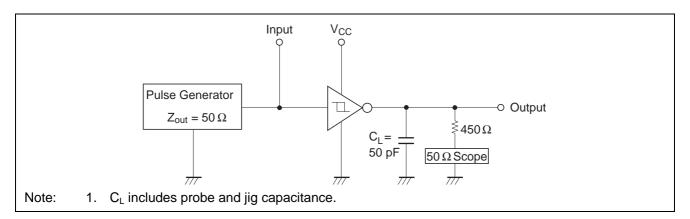
Ta = -40 to 85°C			
Min	Max	Unit	Test Conditions
1.0	2.0	V	

Item	Symbol	V _{CC} (V)	Min	Max	Unit	Test Conditions
Threshold voltage	V _T ⁺	2.7	1.0	2.0	V	
		3.0	1.2	2.2		
		3.6	1.5	2.4		
		4.5	1.6	2.6		
		5.5	2.0	3.0		
	V _T	2.7	0.4	1.4	V	
		3.0	0.6	1.5		
		3.6	0.8	1.8		
		4.5	1.0	2.0		
		5.5	1.4	2.4		
Hysteresis voltage	V _H	2.7	0.3	1.1	V	$V_T^+ - V_T^-$
		3.0	0.4	1.2		
		3.6	0.4	1.2		
		4.5	0.4	1.2		
		5.5	0.4	1.2		
Input voltage	V _{IH}	2.7 to 3.6	2.4	_	V	
		4.5 to 5.5	3.0	_		
	V _{IL}	2.7 to 3.6	_	0.4		
		4.5 to 5.5	_	1.0		
Output voltage	V _{OH}	2.7 to 5.5	V _{CC} -0.2	_	V	I _{OH} = -100 μA
		2.7	2.2	_		$I_{OH} = -12 \text{ mA}$
		3.0	2.4	_		$I_{OH} = -12 \text{ mA}$
		3.0	2.0	_		$I_{OH} = -24 \text{ mA}$
		4.5	3.8	_		$I_{OH} = -24 \text{ mA}$
	V _{OL}	2.7 to 5.5	_	0.2	V	I _{OL} = 100 μA
		2.7	_	0.4		I _{OL} = 12 mA
		3.0	_	0.55		I _{OL} = 24 mA
		4.5	_	0.55		I _{OL} = 24 mA
Input current	I _{IN}	0 to 5.5	_	±5.0	μΑ	V _{IN} = 5.5 V or GND
Quiescent supply current	I _{CC}	5.5	_	20	μΑ	V _{IN} = V _{CC} or GND
	ΔI_{CC}	3.0 to 3.6	_	500	μΑ	V_{IN} = one input at $(V_{CC}-0.6)V$, other inputs at V_{CC} or GND

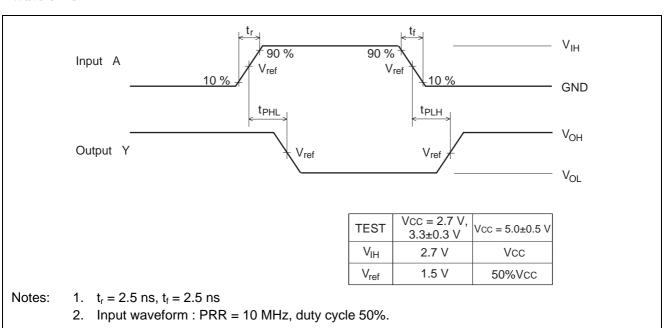
Switching Characteristics

			$Ta = -40 \text{ to } 85^{\circ}C$				From	То
Item	Symbol	V _{CC} (V)	Min	Тур	Max	Unit	(Input)	(Output)
Propagation delay time	t _{PLH}	2.7	_	6.0	9.5	ns	Α	Υ
	t_{PHL}	3.3±0.3	1.5	5.0	8.5			
		5.0±0.5	_	3.5	7.0			
Input capacitance	C _{IN}	2.7	_	3.0	_	pF		
Output capacitance	Co	2.7	_	15.0	_	pF		

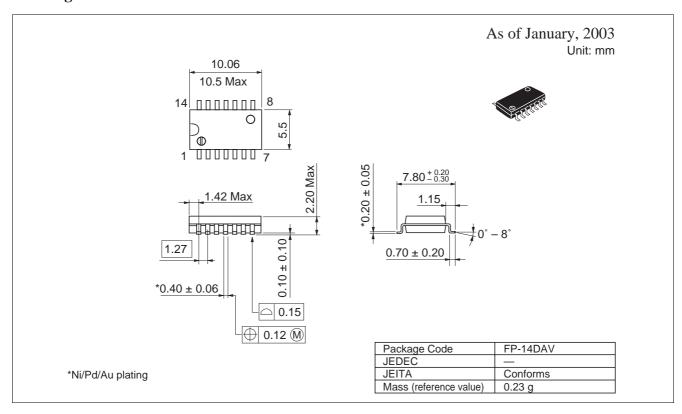
Test Circuit

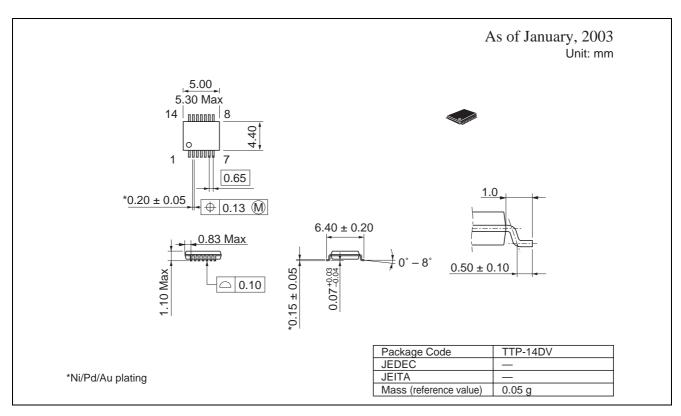


Waveforms



Package Dimensions





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