

New Jersey Semi-Conductor Products, Inc.

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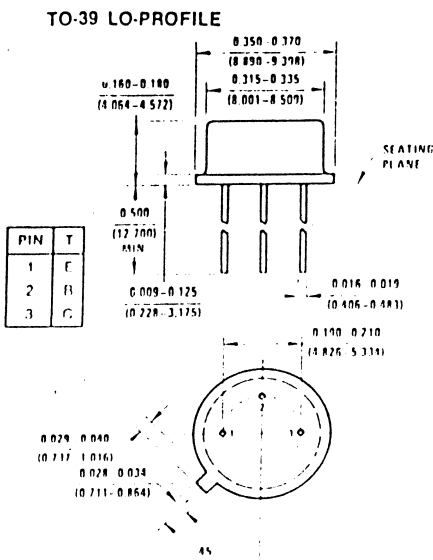
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2N3244 (SILICON)

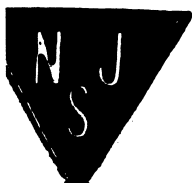
PNP silicon transistors for medium-current, high-speed switching and driver applications.

MAXIMUM RATINGS

Rating	Symbol	2N3244	Unit
Collector-Base Voltage	V_{CB}	40	Vdc
Collector-Emitter Voltage	V_{CEO}	40	Vdc
Emitter-Base Voltage	V_{EB}	5.0	Vdc
Collector Current	I_C	1.0	A _{dc}
Total Device Dissipation @ 25°C Ambient Temperature Derating Factor Above 25°C	P_D	1.0 5.71	Watt mW/°C
Total Device Dissipation @ 25°C Case Temperature Derating Factor Above 25°C	P_D	5.0 28.6	Watts mW/°C
Junction Temperature, Operating	T_J	+200	°C
Storage Temperature Range	T_{stg}	-65 to +200	°C
Thermal Resistance, Junction to Ambient	θ_{JA}	0.175	°C/mW
Thermal Resistance, Junction to Case	θ_{JC}	35	°C/W



PHYSICAL DIMENSIONS



NJ Semi-Conductors reserves the right to change test conditions, parameter limits and package dimensions without notice. Information furnished by NJ Semi-Conductors is believed to be both accurate and reliable at the time of going to press. However NJ Semi-Conductors assumes no responsibility for any errors or omissions discovered in its use. NJ Semi-Conductors encourages customers to verify that data sheets are current before placing orders.

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Fig. No.	Symbol	Min	Max	Unit
Collector Cutoff Current ($V_{CB} = 30 \text{ Vdc}$, $I_E = 0$) ($V_{CB} = 30 \text{ Vdc}$, $I_E = 0$, $T_A = 100^\circ\text{C}$)		I_{CBO}	—	.050 10	$\mu\text{A dc}$
Collector Cutoff Current ($V_{CE} = 30 \text{ Vdc}$, $V_{BE(off)} = 3 \text{ Vdc}$)		I_{CEX}	—	50	nA dc
Emitter-Base Leakage Current ($V_{EB} = 3 \text{ Vdc}$, $I_C = 0$)		I_{EBO}	—	30	nA dc
Base Cutoff Current ($V_{CE} = 30 \text{ Vdc}$, $V_{BE(off)} = 3 \text{ Vdc}$)		I_{BL}	—	80	nA dc
Collector-Base Breakdown Voltage ($I_C = 10 \mu\text{A dc}$, $I_E = 0$)		BV_{CBO}	40	—	Vdc
Collector-Emitter Breakdown Voltage (1) ($I_C = 10 \text{ mA dc}$, $I_B = 0$)		BV_{CEO}	40	—	Vdc
Emitter-Base Breakdown Voltage ($I_E = 10 \mu\text{A dc}$, $I_C = 0$)		BV_{EBO}	5.0	—	Vdc
Collector Saturation Voltage (1) ($I_C = 150 \text{ mA dc}$, $I_B = 15 \text{ mA dc}$) ($I_C = 500 \text{ mA dc}$, $I_B = 50 \text{ mA dc}$) ($I_C = 1 \text{ A dc}$, $I_B = 100 \text{ mA dc}$)	2,3	$V_{CE(sat)}$	—	0.3 0.5 1.0	Vdc
Base-Emitter Saturation Voltage (1) ($I_C = 150 \text{ mA dc}$, $I_B = 15 \text{ mA dc}$) ($I_C = 500 \text{ mA dc}$, $I_B = 50 \text{ mA dc}$) ($I_C = 1 \text{ A dc}$, $I_B = 100 \text{ mA dc}$)	3	$V_{BE(sat)}$	— 0.75 —	1.1 1.5 2.0	Vdc
DC Forward Current Transfer Ratio (1) ($I_C = 150 \text{ mA dc}$, $V_{CE} = 1.0 \text{ Vdc}$) ($I_C = 500 \text{ mA dc}$, $V_{CE} = 1.0 \text{ Vdc}$) ($I_C = 1 \text{ A dc}$, $V_{CE} = 5 \text{ Vdc}$)	1	h_{FE}	60 50 25	— 150 —	—
Output Capacitance ($V_{CB} = 10 \text{ Vdc}$, $I_E = 0$, $f = 100 \text{ kHz}$)	5	C_{ob}	—	25	pF
Input Capacitance ($V_{OB} = 0.5 \text{ Vdc}$, $I_C = 0$, $f = 100 \text{ kHz}$)	5	C_{ib}	—	100	pF
Current-Gain - Bandwidth Product ($I_C = 50 \text{ mA dc}$, $V_{CE} = 10 \text{ Vdc}$, $f = 100 \text{ MHz}$)		f_T	175	—	MHz
Delay Time ($I_C = 500 \text{ mA}$, $I_{B1} = 50 \text{ mA}$ $V_{OB} = 2 \text{ V}$, $V_{CC} = 30 \text{ V}$)	6,8	t_d	—	15	ns
Rise Time		t_r	—	35	ns
Storage Time ($I_C = 500 \text{ mA}$, $V_{CC} = 30 \text{ V}$ $I_{B1} = I_{B2} = 50 \text{ mA}$)	6,9	t_s	—	140	ns
Fall Time		t_f	—	45	ns
Total Control Charge ($I_C = 500 \text{ mA}$, $I_B = 50 \text{ mA}$, $V_{CC} = 30 \text{ V}$)	7,10	Q_T	—	14	nC

(1) Pulse Test: $PW \leq 300 \mu\text{s}$, Duty Cycle $\leq 2\%$