

# 2N2905AHR

### Hi-Rel PNP bipolar transistor 60 V, 0.6 A

Datasheet — production data

#### Features

BV <sub>CEO</sub>	60 V
I <sub>C</sub> (max)	0.6 A
H <sub>FE</sub> at 10 V - 150 mA	> 100
Operating temperature range	-65°C to +200°C

- Hi-Rel PNP bipolar transistor
- Linear gain characteristics
- ESCC qualified
- European preferred part list EPPL
- Radiation level: lot specific total dose contact marketing for specified level

### Description

The 2N2905AHR is a silicon planar epitaxial PNP transistor in a TO-39 package. It is specifically designed for aerospace Hi-Rel applications, and ESCC qualified in accordance with the 5202-002 specification. In case of discrepancies between this datasheet and ESCC detailed specification, the latter prevails.

Table 1.	Device summary	

TO-39

Figure 1. Internal schematic diagram



Order codes	Package	Lead finish	Marking	Туре	EPPL	Packaging
2N2905AHR	TO-39	Gold Solder Dip	520200201 520200202	ESCC Flight	Yes	Strip pack
2N2905AHR	TO-39	Gold	2N2905AT1	Engineering model		Strip pack

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This is information on a product in full production.

# 1 Electrical ratings

Table 2.	Absolute	maximum	ratings
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Symbol	Parameter	Value	Unit
V <sub>CBO</sub>	Collector-base voltage $(I_E = 0)$	-60	V
$V_{CEO}$	Collector-emitter voltage ( $I_B = 0$ )	-60	V
V <sub>EBO</sub>	Emitter-base voltage (I <sub>C</sub> = 0)	-5	V
۱ <sub>C</sub>	Collector current	-0.6	А
P <sub>TOT</sub>	Total dissipation at $T_{amb} \le 25 \ ^{\circ}C$ Total dissipation at $T_c \le 25 \ ^{\circ}C$	0.6 3	W W
T <sub>STG</sub>	Storage temperature	-65 to 200	°C
Τ <sub>J</sub>	Max. operating junction temperature	200	°C

#### Table 3.Thermal data

Symbol	Parameter		Value	Unit
R <sub>thJC</sub>	Thermal resistance junction-case	max	58	°C/W
R <sub>thJA</sub>	Thermal resistance junction-ambient	max	291	°C/W



## 2 Electrical characteristics

 $T_{case}$  = 25 °C unless otherwise specified.

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
I <sub>CBO</sub>	Collector-base cut-off current (I <sub>E</sub> = 0)	V <sub>CB</sub> = -50 V V <sub>CB</sub> = -50 V, T <sub>C</sub> = 150 °C			-10 -10	nA μA
V <sub>(BR)CBO</sub>	Collector-base breakdown voltage $(I_E = 0)$	Ι <sub>C</sub> = -10 μΑ	-60			v
V <sub>(BR)CEO</sub> <sup>(1)</sup>	Collector-emitter breakdown voltage $(I_B = 0)$	I <sub>C</sub> = -10 mA	-60			v
V <sub>(BR)EBO</sub>	Emitter-base breakdown voltage (I <sub>C</sub> = 0)	I <sub>E</sub> = -10 μΑ	-5			v
V <sub>CE(sat)</sub> <sup>(1)</sup>	Collector-emitter saturation voltage	I <sub>C</sub> = -150 mA, I <sub>B</sub> = -15 mA			-0.4	V
V <sub>BE(sat)</sub> <sup>(1)</sup>	Base-emitter saturation voltage	I <sub>C</sub> = -150 mA, I <sub>B</sub> = -15 mA			-1.3	V
h <sub>FE</sub> <sup>(1)</sup>	DC current gain	$I_{C} = -0.1 \text{ mA}, V_{CE} = -10 \text{ V}$ $I_{C} = -1 \text{ mA}, V_{CE} = -10 \text{ V}$ $I_{C} = -150 \text{ mA}, V_{CE} = -10 \text{ V}$ $I_{C} = -500 \text{ mA}, V_{CE} = -10 \text{ V}$	75 100 100 50		300	
h <sub>fe</sub>	Small signal current gain	V <sub>CE</sub> = -20 V, I <sub>C</sub> = -50 mA f = 100 MHz	2			
C <sub>CBO</sub>	Output capacitance (I <sub>E</sub> = 0)	V <sub>CB</sub> = -10 V 100 kHz  ≤ f  ≤1 MHz			8	pF
t <sub>on</sub>	Turn-on time	$V_{CC} = -30 \text{ V}, I_C = -150 \text{ mA}$ $I_{B1} = -15 \text{ mA}$			45	ns
t <sub>off</sub>	Turn-off time	$V_{CC} = -30$ V, $I_C = -150$ mA $I_{B1} = -I_{B2} = -15$ mA			300	ns

 Table 4.
 Electrical characteristics

1. Pulsed duration = 300  $\mu$ s, duty cycle  $\leq 1.5\%$ 









### 2.2 Test circuit





1. Fast electronic switch

2. Non-inductive resistor



### 3 Package mechanical data

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Table 5.	TO-39	mechanical	data
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Dim.		mm	
	Min.	Тур.	Max.
A		12.70	14.20
В		0.40	0.49
С		0.58	0.74
D		6.00	6.40
E	_	8.15	8.25
F		9.10	9.20
G		4.93	5.23
Н		0.85	0.95
I		0.75	0.85
L		42°	48°

#### Figure 7. TO-39 drawing



## 4 Revision history

#### Table 6.Document revision history

Date	Revision	Changes
14-Jan-2009	1	Initial release
05-Jan-2010	2	Modified Table 1 on page 1
04-Oct-2012	3	Minor text changes. Section 2.1: Electrical characteristics (curves) has been added.



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Doc ID 15295 Rev 3

