

Vishay Semiconductors

Small Signal Schottky Diodes

Features

- For general purpose applications
- The SD101 series is a Metal-on-silicon Schottky barrier device which is protected by a PN junction guard ring
- The low forward voltage drop and fast switching make it ideal for protection of MOS devices, steering, biasing and coupling diodes for fast switching and low logic level applications
- These diodes are also available in the Mini-MELF case with type designations LL101A to LL101C, in the DO-35 case with type- designations SD101A to SD101C and in the SOD-123 case with type designations SD101AW-V to SD101CW-V.
- AEC-Q101 qualified
- Compliant to RoHS directive 2002/95/EC and in accordance to WEEE 2002/96/EC



Mechanical Data

Case: SOD-323 Weight: approx. 4.3 mg Packaging Codes/Options:

GS18/10 k per 13" reel (8 mm tape), 10 k/box GS08/3 k per 7" reel (8 mm tape), 15 k/box

Parts Table

Part	Ordering code	Type Marking	Remarks
SD101AWS-V	SD101AWS-V-GS18 or SD101AWS-V-GS08	SA	Tape and Reel
SD101BWS-V	SD101BWS-V-GS18 or SD101BWS-V-GS08	SB	Tape and Reel
SD101CWS-V	SD101CWS-V-GS18 or SD101CWS-V-GS08	SC	Tape and Reel

Absolute Maximum Ratings

T_{amb} = 25 °C, unless otherwise specified

Parameter	Test condition	Part	Symbol	Value	Unit
		SD101AWS-V	V _{RRM}	60	V
Peak inverse voltage		SD101BWS-V	V _{RRM}	50	V
		SD101CWS-V	V _{RRM}	40	V
Power dissipation (Infinite Heat Sink)			P _{tot}	150 ¹⁾	mW
Forward continuous current			١ _F	30	mA
Maximum single cycle surge	10 µs square wave		I _{FSM}	2	A

¹⁾ Valid provided that electrodes are kept at ambient temperature

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Thermal Characteristics

T_{amb} = 25 °C, unless otherwise specified

Parameter	Test condition	Symbol	Value	Unit	
Thermal resistance junction to ambient air		R _{thJA}	650 ¹⁾	K/W	
Junction temperature		Тj	125 ¹⁾	°C	
Storage temperature range		T _{stg}	- 65 to + 150	°C	

¹⁾ Valid provided that electrodes are kept at ambient temperature

Electrical Characteristics

T_{amb} = 25 °C, unless otherwise specified

Parameter	Test condition	Part	Symbol	Min	Тур.	Max	Unit
Reverse breakdown voltage	I _R = 10 μΑ	SD101AWS-V	V _(BR)	60			V
		SD101BWS-V	V _(BR)	50			V
		SD101CWS-V	V _(BR)	40			V
Leakage current	V _R = 50 V	SD101AWS-V	I _R			200	nA
	V _R = 40 V	SD101BWS-V	I _R			200	nA
	V _R = 30 V	SD101CWS-V	I _R			200	nA
Forward voltage drop	I _F = 1 mA	SD101AWS-V	V _F			410	mV
		SD101BWS-V	V _F			400	mV
		SD101CWS-V	V _F			390	mV
	I _F = 15 mA	SD101AWS-V	V _F			1000	mV
		SD101BWS-V	V _F			950	mV
		SD101CWS-V	V _F			900	mV
Junction capacitance	V _R = 0 V, f = 1 MHz	SD101AWS-V	CD			2.0	ns
		SD101BWS-V	CD			2.1	ns
		SD101CWS-V	CD			2.2	ns
Reverse recovery time	$I_F = I_R = 5 \text{ mA},$ recover to 0.1 I_R		t _{rr}			1	ns

Typical Characteristics

 $T_{amb} = 25 \text{ °C}$, unless otherwise specified

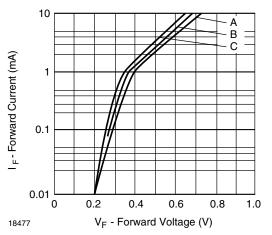


Figure 1. Typical Variation of Forward Current vs. Forward Voltage

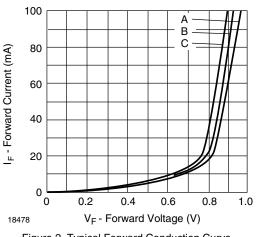


Figure 2. Typical Forward Conduction Curve



SD101AWS-V, SD101BWS-V,

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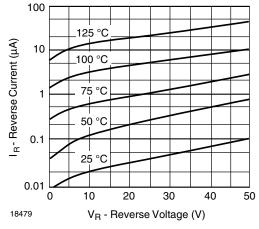


Figure 3. Typical Variation of Reverse Current at Various Temperatures

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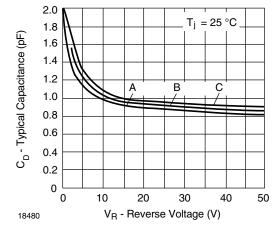
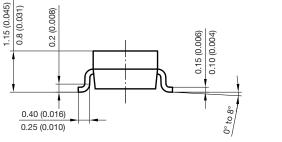
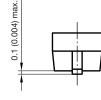
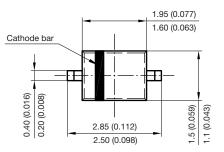


Figure 4. Typical Capacitance Curve as a Function of Reverse Voltage

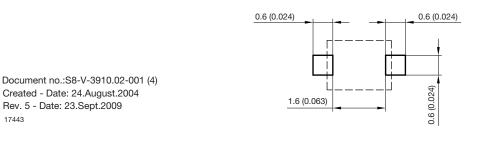








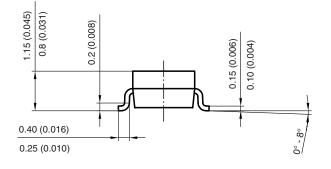
Foot print recommendation:

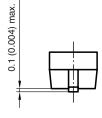


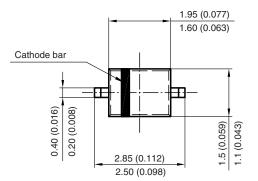
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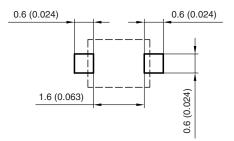
PACKAGE DIMENSIONS in millimeters (inches)







Created - Date: 24. August. 2004 Rev. 04 - Date: 23. Sep .2009 Document no.: S8-V-3910.02-001 (4) 17443 Mounting pad layout





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