QUICKSWITCH® PRODUCTS HIGH-PERFORMANCE CMOS ANALOG FOUR-CHANNEL SPST SWITCH WITH INDIVIDUAL ENABLES

IDTQS4A101

FEATURES:

- Low ON resistance: $r_{DS(ON)}$ = 5Ω
- Wide bandwidth: 1.4GHz (-3dB point)
- Crosstalk: 122dB at 50KHz, -80dB at 5MHz, -65dB at 30MHz
- Off-isolation: -100dB at 50KHz, -75dB at 5MHz, -45dB at 30MHz
- Single 5V supply
- Bidirectional signal flow
- TTL-compatible control inputs
- Ultra-low quiescent current: 3µA
- Switch turn on time of 6.5ns
- Available in QSOP package

APPLICATIONS:

- High-speed video signal switching/routing
- Audio signal switching/routing
- Data acquisition
- ATE systems
- Telecomm routing
- Token Ring transceivers
- High-speed networking

DESCRIPTION:

The QS4A101 is a high-performance CMOS analog four-channel SPST switch with individual enables. This device provides a set of four high-speed CMOS switches connecting inputs to outputs. The low ON resistance of the QS4A101 allows inputs to be connected to outputs with low insertion loss and high bandwidth.

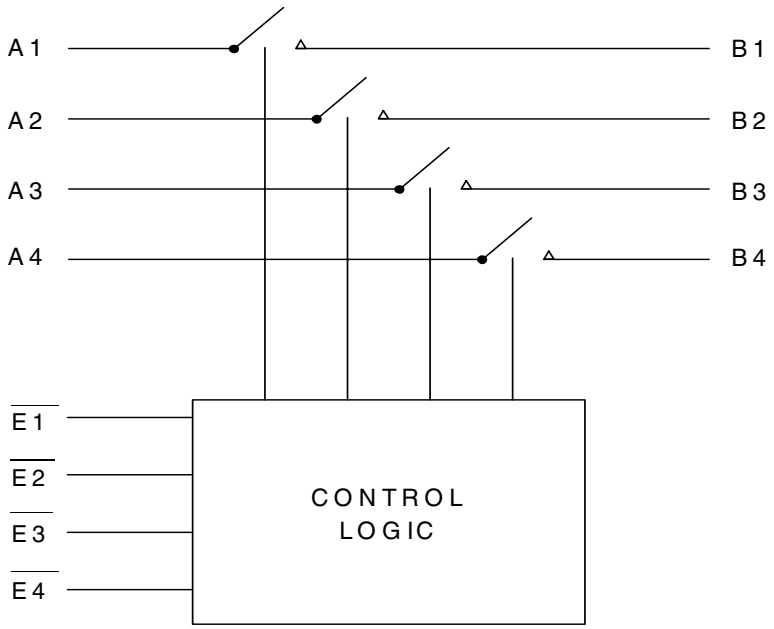
The QS4A101, with 1.4GHz bandwidth, is ideal for high-performance video signal switching, audio signal switching, and telecomm routing applications. Low power dissipation makes this device ideal for battery operated and remote instrumentation applications.

The QS4A101 is offered in the QSOP package which has several advantages over conventional packages such as PDIP and SOIC, including:

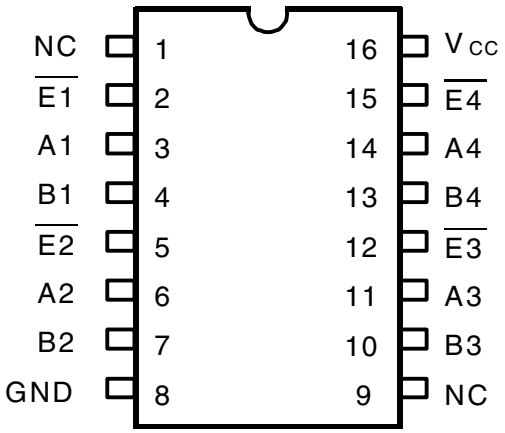
- Reduced signal delays due to denser component packaging on circuit boards
- Reduced system noise due to less pin inductance

The QS4A101 is characterized for operation at -40°C to +85°C.

FUNCTIONAL BLOCK DIAGRAM



PIN CONFIGURATION



QSOP
TOP VIEW

ABSOLUTE MAXIMUM RATINGS(1)

Symbol	Description	Max	Unit
V_{TERM}(2)	Supply Voltage to Ground	–0.5 to +7	V
V_{TERM}(3)	DC Switch Voltage Vs	–0.5 to +7	V
—	Analog Input Voltage	–0.5 to +7	V
V_{TERM}(3)	DC Input Voltage V_{IN}	–0.5 to +7	V
V_{AC}	AC Input Voltage (pulse width ≤20ns)	–3	V
I_{OUT}	DC Output Current	120	mA
P_{MAX}	Maximum Power Dissipation	0.7	W
T_{STG}	Storage Temperature	–65 to +150	° C

NOTES:
1. Stresses greater than those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.
2. V_{CC} terminals.
3. All terminals except V_{CC} .

PIN DESCRIPTION

Pin Names	I/O	Description
A_1 -A_4	I/O	Port A
B_1 -B_4	I/O	Port B
$\overline{E}_1$ -$\overline{E}_4$	I	Port Switch Enable

FUNCTION TABLE(1)

$\overline{E}$	A	B	Function
L	H	H	Connect
L	L	L	Connect
H	X	X	Disconnect

NOTE:
1. H = HIGH Voltage Level
   L = LOW Voltage Level
   X = Don't Care

POWER SUPPLY CHARACTERISTICS

Symbol	Parameter	Test Conditions	Max.	Unit
I_{CC}	Supply Current	V_{CC} = Max., V_{IN} = GND or V_{CC}	3	µA

DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE

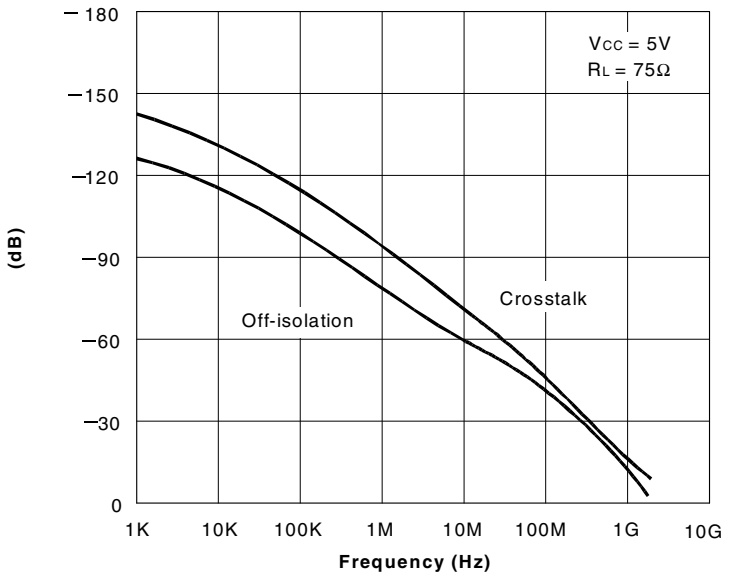
Following Conditions Apply Unless Otherwise Specified:
Industrial: T_A = –40°C to +85°C, V_{CC} = 5V ± 5%

Symbol	Parameter	Test Conditions	Min.	Typ.[1]	Max.	Unit
Analog Switch						
V_{IN}	Analog Signal Range[2]		-0.5	1	Vcc - 1	V
$r_{DS(ON)}$	Drain-source ON resistance[2,3]	Vcc = Min., V_{IN} = 0V, I_{ON} = 30mA	—	5	7	Ω
		Vcc = Min., V_{IN} = 2.4V, I_{ON} = 15mA	—	13	17	
$I_{C(OFF)}$	Channel Off Leakage Current	A = Vcc or 0V, B = 0V or Vcc, $\overline{E}$ = Vcc	—	1	—	nA
$I_{C(ON)}$	Channel On Leakage Current	A = B = 0V (each channel is turned on sequentially)	—	1	—	nA
Digital Control						
V_{IH}	Input HIGH Voltage	Guaranteed Logic HIGH for Control Pins	2	—	—	V
V_{IL}	Input LOW Voltage	Guaranteed Logic LOW for Control Pins	—	—	0.8	V
Dynamic Characteristics						
$t_{ON(\overline{E})}$	Enable Turn-On Time $\overline{E}$ to A, B	R_L = 1KΩ, C_L = 100pF (See Switching Time)	0.5	—	6.5	ns
$t_{OFF(\overline{E})}$	Enable Turn-Off Time $\overline{E}$ to A, B	R_L = 1KΩ, C_L = 100pF (See Switching Time)	0.5	—	6	ns
t_{PD}	Group Delay[2,4a]	R_L = 1KΩ, C_L = 100pF	—	—	250	ps
f_{3dB}	-3dB Bandwidth	V_{IN} = 0 to 1V, 1Vp-p, R_L = 75Ω	—	1.4	—	GHz
	Off-isolation	V_{IN} = 0 to 1V, 1Vp-p, R_L = 75Ω, f = 5.5MHz	—	-80	—	dB
X_{TALK}	Crosstalk	V_{IN} = 0 to 1V, 1Vp-p, R_L = 75Ω, f = 30MHz	—	-75	—	dB
$C_{(OFF)}$	Switch Off Capacitance	$\overline{E}$ = Vcc, V_{IN} = V_{OUT} = 0V	—	5	—	pF
$C_{(ON)}$	Switch On Capacitance	$\overline{E}$ = 0V, V_{IN} = V_{OUT} = 0V	—	10	—	pF
Q_{CI}	Charge Injection		—	1.5	—	pC

NOTES:

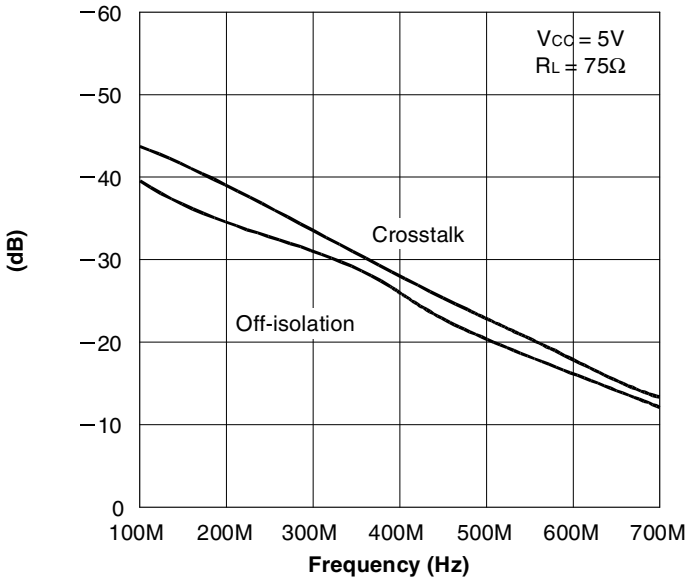
1. Typical values are at V_{CC} = 5.0V, T_A = 25°C.
2. Max value is guaranteed but not production tested.
3. Measured by voltage drop between A and C pins at indicated current through the switch ON resistance is determined by the lower of the voltages on the two (A, B) pins.
4. The bus switch contributes no group delay other than the RC delay of the ON resistance of the switch and load capacitance. Group delay of the bus switch, when used in a system, is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

TYPICAL CHARACTERISTICS



Off-isolation and Crosstalk vs. Frequency

NOTES:
1. Crosstalk = 20 log |Vo/Vs|
2. Off-isolation = 20 log |Vo/Vs|



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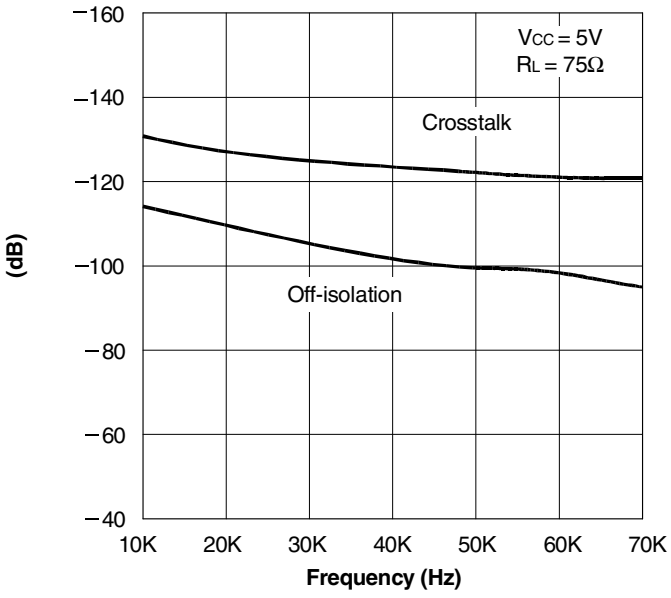

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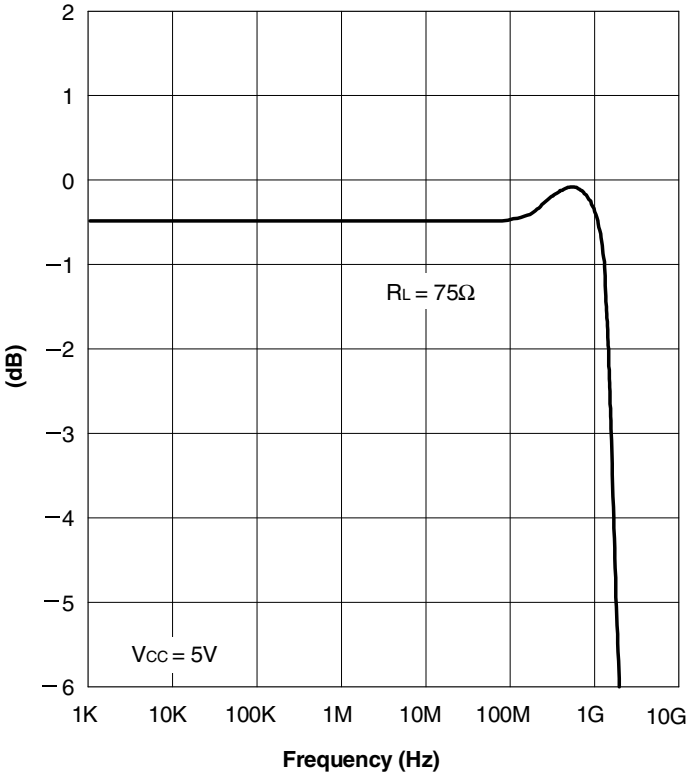
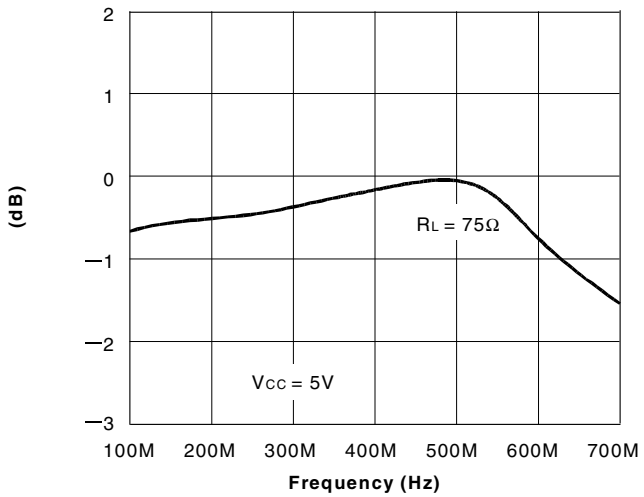


Insertion Loss vs. Frequency

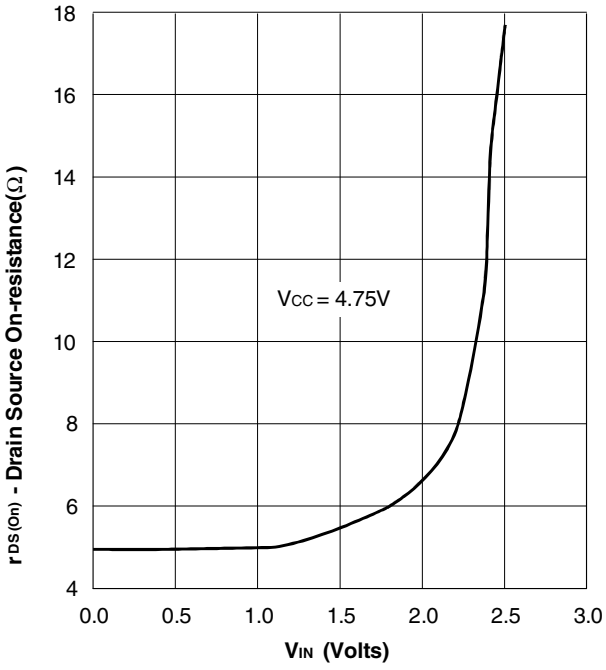
NOTE:
1. Insertion Loss = 20 log |Vo/Vs|

TYPICAL CHARACTERISTICS (CONTINUED)



Insertion Loss vs. Frequency

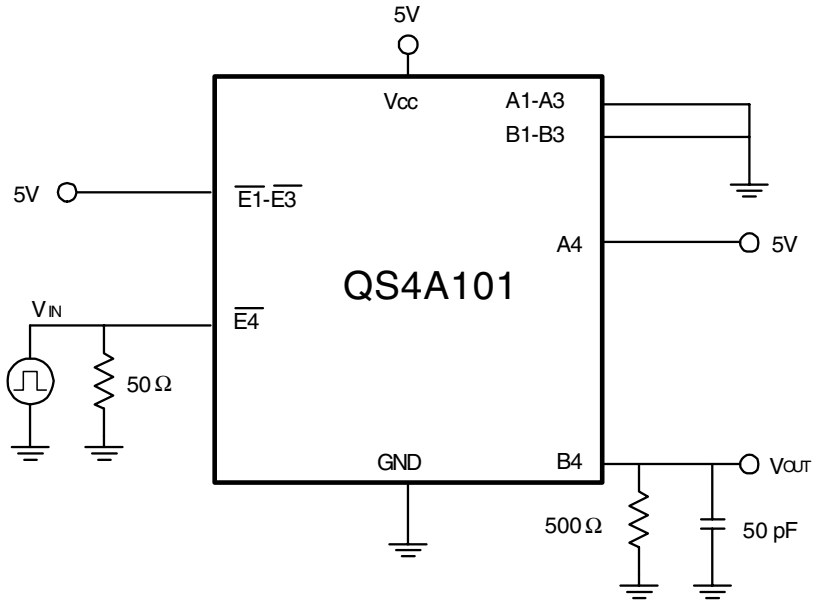
NOTE:
1. Insertion Loss = 20 log |Vo/Vs|

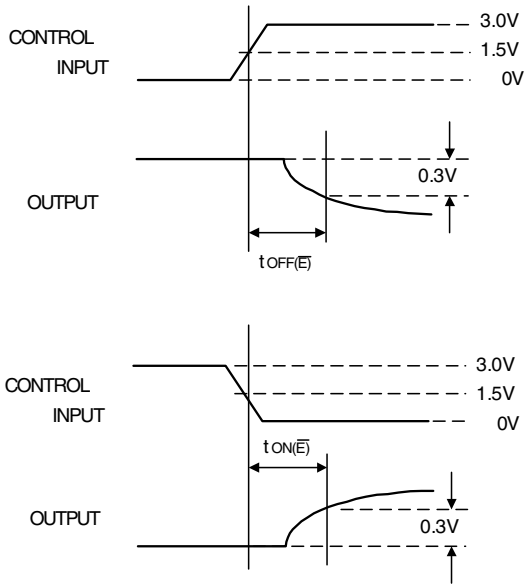


RON LINK

On-Resistance vs. VIN

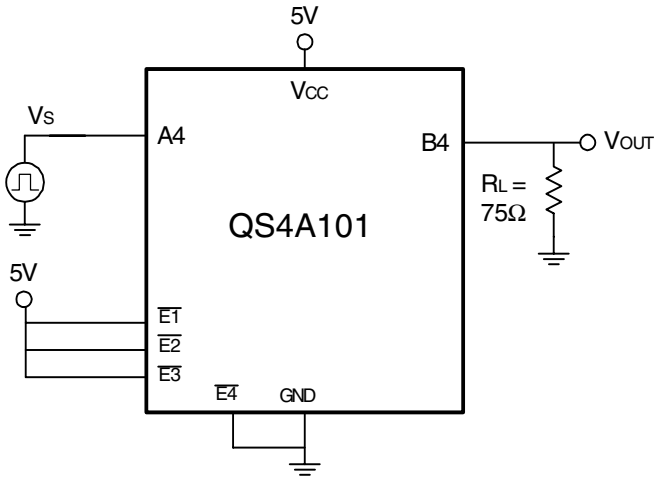
TEST CIRCUITS





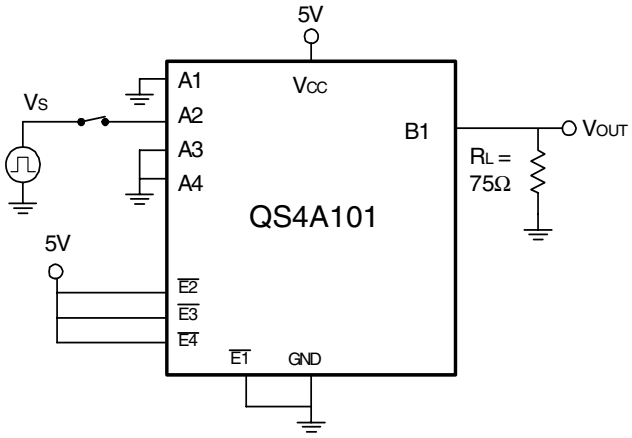
Switching Time

TEST CIRCUITS (CONTINUED)



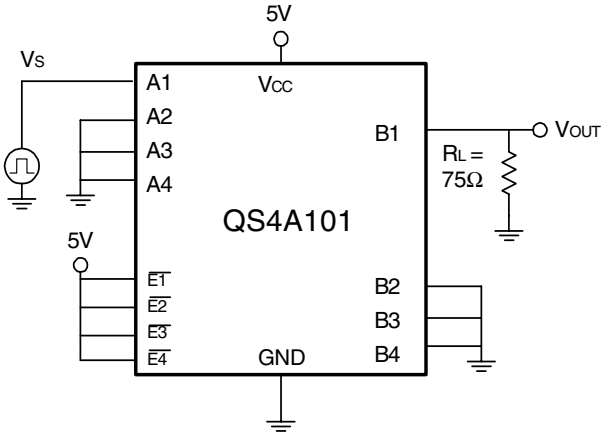
Insertion Loss

NOTES:
1. Insertion Loss = 20 log |Vo/Vs|
2. All unused pins are grounded.



Crosstalk

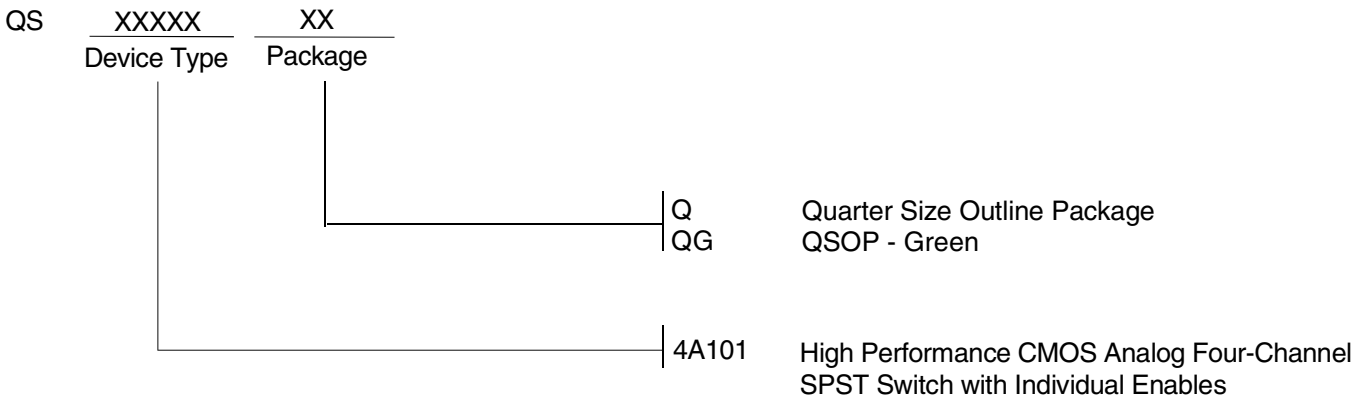
NOTES:
1. Crosstalk = 20 log |Vo/Vs|
2. All unused pins are grounded.



Off-Isolation

NOTE:
1. Off-isolation = 20 log |Vo/Vs|

ORDERING INFORMATION



CORPORATE HEADQUARTERS
6024 Silver Creek Valley Road
San Jose, CA 95138

for SALES:
800-345-7015 or 408-284-8200
fax: 408-284-2775
www.idt.com

for Tech Support:
logichelp@idt.com