



PJP13N50 / PJF13N50

500V N-Channel Enhancement Mode MOSFET

TO-220AB / ITO-220AB

FEATURES

- 13A , 500V, $R_{DS(ON)}=0.52\Omega@V_{GS}=10V, I_D=6.5A$
- Low ON Resistance
- Fast Switching
- Low Gate Charge
- Fully Characterized Avalanche Voltage and Current
- Specially Designed for AC Adapter, Battery Charge and SMPS
- In compliance with EU RoHs 2002/95/EC Directives

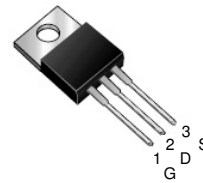
MECHANICAL DATA

- Case: TO-220AB / ITO-220AB Molded Plastic
- Terminals : Solderable per MIL-STD-750,Method 2026

ORDERING INFORMATION

TYPE	MARKING	PACKAGE	PACKING
PJP13N50	P13N50	TO-220AB	50PCS/TUBE
PJF13N50	F13N50	ITO-220AB	50PCS/TUBE

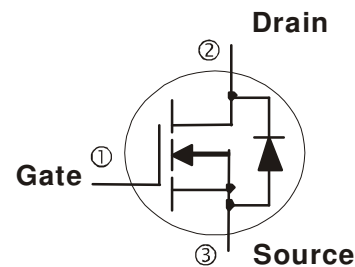
TO-220AB



ITO-220AB



INTERNAL SCHEMATIC DIAGRAM



Maximum RATINGS and Thermal Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	Symbol	PJP13N50	PJF13N50	Units
Drain-Source Voltage	V_{DS}	500		V
Gate-Source Voltage	V_{GS}	± 30		V
Continuous Drain Current	I_D	13	13	A
Pulsed Drain Current ¹⁾	I_{DM}	52	52	A
Maximum Power Dissipation Derating Factor	P_D	175 1.4	52 0.42	W
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 to +150		$^\circ\text{C}$
Avalanche Energy with Single Pulse $I_{AS}=12.5A, V_{DD}=50V, L=10mH$	E_{AS}	780		mJ
Junction-to-Case Thermal Resistance	$R_{\theta JC}$	0.7	2.4	$^\circ\text{C/W}$
Junction-to Ambient Thermal Resistance	$R_{\theta JA}$	62.5	100	$^\circ\text{C/W}$

Note: 1. Maximum DC current limited by the package

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ELECTRICAL CHARACTERISTICS (T_A=25°C unless otherwise noted)

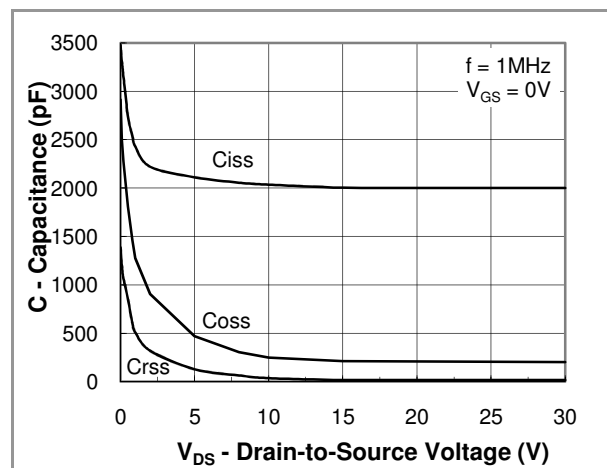
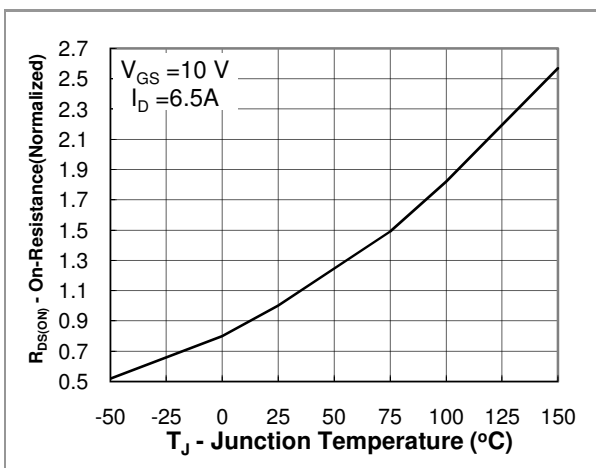
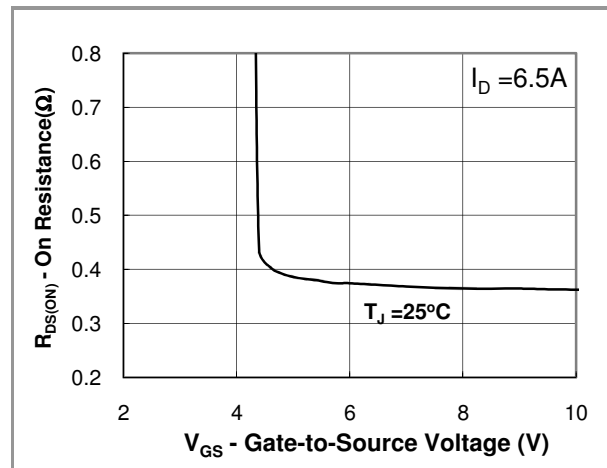
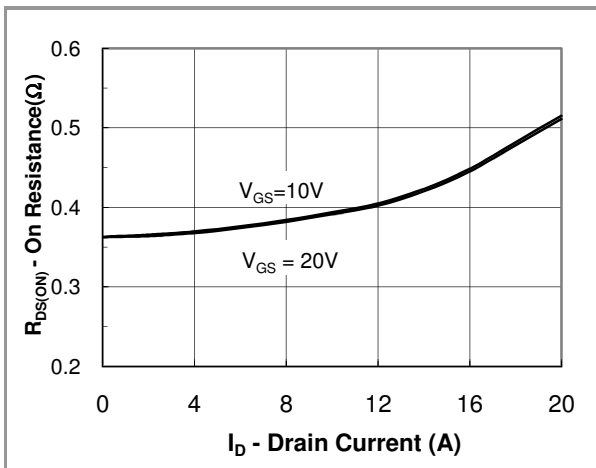
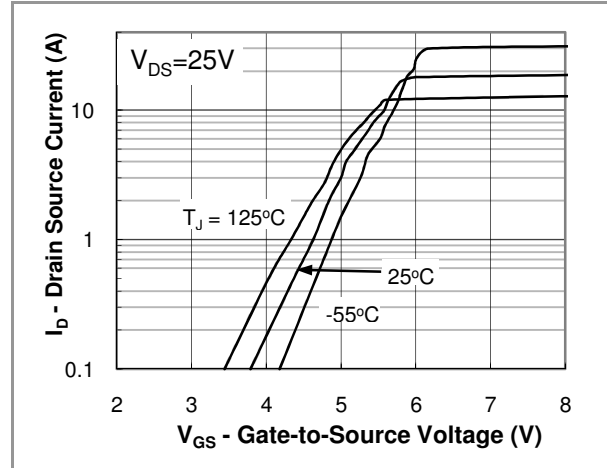
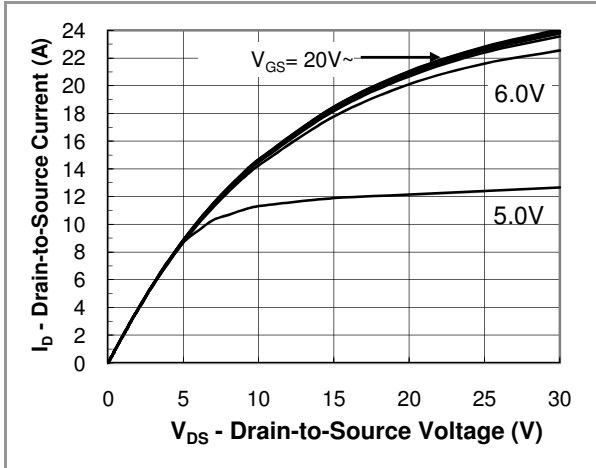
Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Units
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	500	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250uA	2.0	-	4.0	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =10V, I _D =6.5A	-	0.36	0.52	Ω
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =500V, V _{GS} =0V	-	-	1	uA
Gate Body Leakage	I _{GSS}	V _{GS} =±30V, V _{DS} =0V	-	-	±100	nA
Dynamic						
Total Gate Charge	Q _g	V _{DS} =400V, I _D =12A V _{GS} =10V	-	58.6	-	nC
Gate-Source Charge	Q _{gs}		-	11.8	-	
Gate-Drain Charge	Q _{gd}		-	18.6	-	
Turn-On Delay Time	t _{d(on)}	V _{DD} =250V, I _D =6A V _{GS} =10V, R _G =25Ω	-	19.6	32	ns
Turn-On Rise Time	t _r		-	42	85	
Turn-Off Delay Time	t _{d(off)}		-	80.4	150	
Turn-Off Fall Time	t _f		-	52	90	
Input Capacitance	C _{iss}	V _{DS} =25V, V _{GS} =0V f=1.0MHz	-	2000	2450	pF
Output Capacitance	C _{oss}		-	205	250	
Reverse Transfer Capacitance	C _{rss}		-	16	22	
Source-Drain Diode						
Max. Diode Forward Current	I _S	-	-	-	13	A
Max.Pulsed Source Current	I _{SM}	-	-	-	52	A
Diode Forward Voltage	V _{SD}	I _S =13A, V _{GS} =0V	-	-	1.4	V
Reverse Recovery Time	t _{rr}	V _{GS} =0V, I _F =12A di/dt=100A/us	-	450	-	ns
Reverse Recovery Charge	Q _{rr}		-	5.0	-	uC

NOTE : Plus Test : Pluse Width ≤ 300us, Duty Cycle ≤ 2%.



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Typical Characteristics Curves ($T_a=25^\circ\text{C}$, unless otherwise noted)





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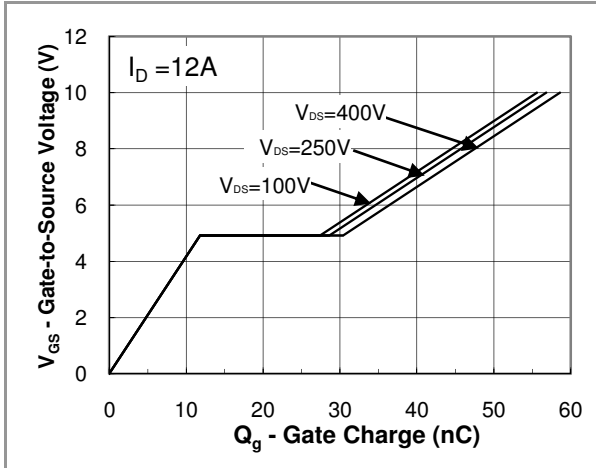


Fig. 7 Gate Charge Waveform

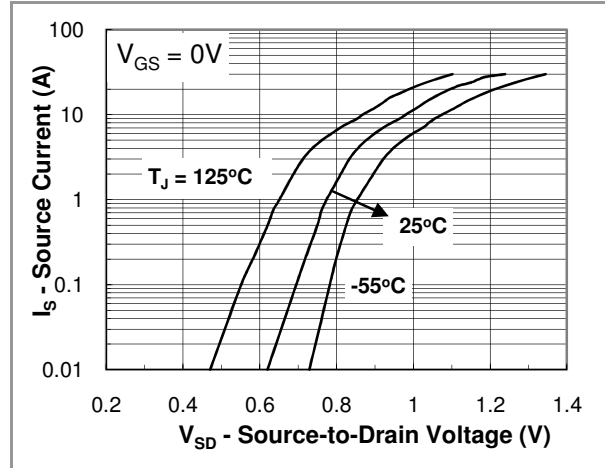


Fig.8 Source-Drain Diode Forward Voltage

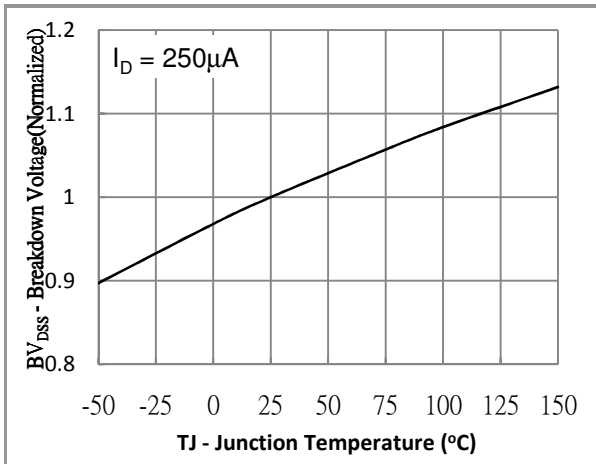


Fig.9 Breakdown Voltage vs Junction Temperature



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