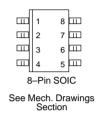


DS2502/5/6-UNW UniqueWareTM Add-Only Memory

SPECIAL FEATURES

- 1024 bits, 16K bits or 64K bits Electrically Programmable Read Only Memory (EPROM) communicates with the economy of one signal plus ground
- Unique, factory-lasered and tested 64-bit registration number (8-bit family code, 36-bit serialization, 12-bit UniqueWare Identifier 5E7H, 8-bit CRC-tester) assures absolute traceability because no two parts are
- Built-in multidrop controller ensures compatibility with other MicroLAN products
- EPROM partitioned into two 256-bit pages for randomly accessing packetized data records
- Each memory page can be permanently write-protected to prevent tampering
- Device is an "add only" memory where additional data can be programmed into EPROM without disturbing existing data
- Reduces control, address, data, power and programming signals to a single pin
- Directly connects to a single port pin of a microprocessor and communicates at up to 16.3k bits per second
- Presence detector acknowledges when reader first applies voltage
- Low cost TO-92/PR-35 or 8-pin SOIC and TSOC surface mount packages
- Reads over a wide voltage range of 2.8V to 6.0V from -40°C to +85°C

TO-92/PR-35 2 **BOTTOM VIEW** See Mech. Drawings Section





TOP VIEW 3.7 X 4.0 X 1.5 mm See Mech. Drawings Section

PIN ASSIGNMENT

TO-92/PR-35	TSOC	SOIC
Ground	Ground	NC
Data	Data	NC
NC	NC	Data
	NC	Ground
	NC	NC
	Ground Data	Data Data NC NC — NC

SILICON LABEL DESCRIPTION

UniqueWare Add-Only Memories are factory programmed versions of the DS2502 (1024 bit), the DS2505 (16K bit) and the DS2506 (64K bit) Add-Only Memories, respectively. They differ from the regular devices in their custom ROM family codes (see Ordering Information) and the UniqueWare Identifier 5E7 in place of the upper 12 bits of the standard serialization field. For technical details on the devices please refer to the DS2502, DS2505 and DS2506 data sheets.

UniqueWare Add-Only Memories are only available preprogrammed with customer-specific and write-protected data. UniqueWare data fills at least one but no more than the first four pages of a device, depending on the length of the customer-supplied data. This leaves up to three (DS2502-UNW), 63 (DS2505-UNW) or 255 (DS2506-UNW) memory pages available for programming in the application.

For more details on UniqueWare and how to set up data files, please refer to the UniqueWare Project Setup Manual, available as Application Note 99 from Dallas Semiconductor. The UniqueWare Project Setup Software is availble from the Dallas Semiconductor FTP Site at

ftp://ftp.dalsemi.com/pub/auto id, unwsetup.exe.

ORDERING INFORMATION

Memory Size 1024 bits (4 pages)	Family Code 89h	Package TO-92 package 8-pin 150 mil SOIC pkg. 6-pin TSOC package	Ordering Part Number DS2502-UNW-pppp DS2502S-UNW-pppp DS2502P-UNW-pppp
16K bits	8Bh	TO-92 package	DS2505–UNW–pppp
(64 pages)		6-pin TSOC package	DS2505P–UNW–pppp
64K bits	8Fh	PR-35 package	DS2506-UNW-pppp
(256 pages)		8-pin 208 mil SOIC pkg.	DS2506S-UNW-pppp

pppp stands for the Project ID assigned to each individual data pattern at the time of the first order.

Sample UniqueWare Data Structures

SAMPLE 1: ETHERNET NODE ADDRESS Figure 1a

(unused)	CRC16		Compan	y ID Value	Extensi	on ID Value	Proje	ct ID	Length				
	MSB	LSB	MSB	LSB	MSB	LSB	MSB	LSB					
19 bytes FFH	2 bytes		3 bytes constant assigned by IEEE		3 bytes	serialization	4 by cons		1 byte 0AH				
high addres	SS								low address				

PHYSICAL ADDRESS AND DATA MAPPING Figure 1b

ADDRESS	0C	0B	0A	09	08	07	06	05	04	03	02	01	00
DATA	xx	xx	ch	cm	cl	hh	mm	Ш	00	00	рр	рр	0A

xx xx = CRC16, value depends on actual data

ch cm cl = high, medium and low byte of the IEEE assigned "Company ID"

hh mm II = high, medium and low byte of the "Extension ID" or serialization

pp pp = Project ID assigned by Dallas Semiconductor

SAMPLE 2: EUI-64 FireWireTM NODE ADDRESS Figure 2a

(unused)	CRC16		Compan	y ID Value	Extensi	on ID Value	Proje	ct ID	Length
	MSB	LSB	MSB	LSB	MSB	LSB	MSB	LSB	
17 bytes FFH	2 bytes		3 bytes constant assigned by IEEE		5 bytes	serialization	4 by cons		1 byte 0CH
high addres	SS								low address

PHYSICAL ADDRESS AND DATA MAPPING Figure 2b

ADDRESS	0E	0D	0C	0B	0A	09	08	07	06	05	04	03	02	01	00
DATA	xx	xx	ch	cm	cl	hh	hm	mm	ml	=	00	00	pp	рр	0C

xx xx = CRC16, value depends on actual data

ch cm cl = high, medium and low byte of the IEEE assigned "Company ID"

hh hm mm ml II = high, medium and low byte of the "Extension ID" or serialization

pp pp = Project ID assigned by Dallas Semiconductor

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