Security & Chip Card ICs

SLE 66CX80S

16-bit Security Controller with
32-Kbyte ROM, 1280 (+ 700) Byte RAM
8-Kbyte EEPROM and
1100-bit Advanced Crypto Engine

Short Product Information 06.99

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Revision History:	**Current Version 06.99**
Previous Releases:	1.2 (31.08.98)
Page	Subjects (changes since last revision)
	Layout change

Important:	Further information is confidential and on request. Please contact: Infineon Technologies AG in Munich, Germany, Security & Chip Card ICs, Fax +49 89 234-28925

Attention please!

The information herein is given to describe certain components and shall not be considered as warranted characteristics.

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Infineon Technologies is an approved CECC manufacturer.

Information

For further information on technology, delivery terms and conditions and prices please contact your nearest Infineon Technologies Office in Germany or our Infineon Technologies Representatives world-wide (see address list).

Warnings

Due to technical requirements components may contain dangerous substances. For information on the types in question please contact your nearest Infineon Technologies Office.

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**16-bit Security Controller SLE 66CX80S with
32-Kbyte ROM, 1280 (+700) Byte RAM,
8-Kbyte EEPROM and 1100-bit Advanced Crypto Engine**

Features

- 16-bit microcomputer in 0.6 µm CMOS technology
- Instruction set opcode compatible with standard SAB 8051 processor
- Enhanced 16-bit arithmetic
- Additional powerful instructions optimized for chip card applications
- Dedicated, non-standard architecture with **execution time six times faster** than standard SAB 8051 processor
- **31.5-Kbytes User ROM** for application programs
- 512-bytes reserved ROM for Resource Management System (RMS) with intelligent write/erase routines
- 8-Kbytes EEPROM as program/data memory
- 256 (+ 700) bytes internal RAM
- **1-Kbyte external RAM** (XRAM)
- **1100-bit Advanced Crypto Engine** (ACE) for fast execution of public key crypto algorithms
- **True random number generator**
- **Interrupt module for I/O interface**
- **CRC Module**
- **16-bit timer with 8-bit prescaler**
- Power saving sleep mode
- Clock freq. = int. freq.: 1 to 7.5 MHz
- Contact configuration and serial interface in accordance with ISO 7816
- Supply voltage range: 2.7 V to 5.5 V
- Current consumption < 10 mA at 5 MHz and 5.5 V
- Temperature range: -25 to +70°C
- ESD protection larger than 4 kV
- Software compatible with SLE 44CR80S

EEPROM

- Reading, erasing and writing byte by byte
- Flexible page mode for 1 to 32 bytes write/erase operation
- 32 bytes security area
- Write time 3.6 ms, erase time 1.8 ms
- Programming time adaptable to clock frequency
- **Minimum of 500,000 write/erase cycles**
- Data retention for a minimum of ten years
- EEPROM programming voltage generated on chip

Security Features

- ROM code not visible due to implantation
- Low and high voltage sensors
- Low-frequency sensor
- High-frequency filter
- Internal power-on-reset
- 16 bytes security PROM, hardware protected
- Unique chip identification number for each chip
- Security optimized layout
- Additional security features

Support

- Tools
- Application notes (e.g.: T=0, T=1, DES, RSA, ACE library etc.)

Features (cont'd)

Enhanced Crypto Performance

Operation	Modulus	Exponent	Calculation Time at 5 MHz
Modular Exponentiation	160 bit	160 bit	20 ms
Modular Exponentiation	256 bit	256 bit	35 ms
Modular Exponentiation	512 bit	512 bit	110 ms
Modular Exponentiation RSA Encrypt / RSA Signature Verify	1024 bit	16 bit	20 ms
Modular Exponentiation RSA Decrypt / RSA Signature Generate	1024 bit	1024 bit	820 ms
Modular Exponentiation using CRT RSA Decrypt / RSA Signature Generate	eq.1024 bit	eq.1024 bit	250 ms
DSA Signature Generate	512 bit	160 bit	145 ms
DSA Signature Verify	512 bit	160 bit	130 ms
DSA Signature Generate	1024 bit	160 bit	290 ms
DSA Signature Verify	1024 bit	160 bit	360 ms
Elliptic Curves EC-GDSA Sign. Generate	160 bit	160 bit	260 ms
Elliptic Curves EC-GDSA Sign. Verify.	160 bit	160 bit	550 ms

Ordering Information

Type	Package[1]	Voltage Range	Temperature Range	Frequency Range
SLE 66CX80S-M6	M6	2.7 V - 5.5 V	– 25°C to + 70°C	1 MHz - 5 MHz @ 5V 1 MHz - 4 MHz @ 3V
SLE 66CX80S-C	C			
SLE 66CX80S-T85-M6	M6	2.7 V - 5.5 V	– 25°C to + 85°C	1 MHz - 5 MHz @ 5V 1 MHz - 4 MHz @ 3V
SLE 66CX80S-T85-C	C			
SLE 66CX80S-V5-M6	M6	4.5 V - 5.5 V	– 25°C to + 70°C	1 MHz - 5 MHz
SLE 66CX80S-V5-C	C			
SLE 66CX80S-V5-T85-M6	M6	4.5 V - 5.5 V	– 25°C to + 85°C	1 MHz - 5 MHz
SLE 66CX80S-V5-T85-C	C			
SLE 66CX80S-V5-F7-M6	M6	4.5 V - 5.5 V	– 25°C to + 70°C	1 MHz - 7.5 MHz
SLE 66CX80S-V5-F7-C	C			

[1] available as wire-bonded module (M6) for embedding in plastic cards or as die (C) for customer packaging
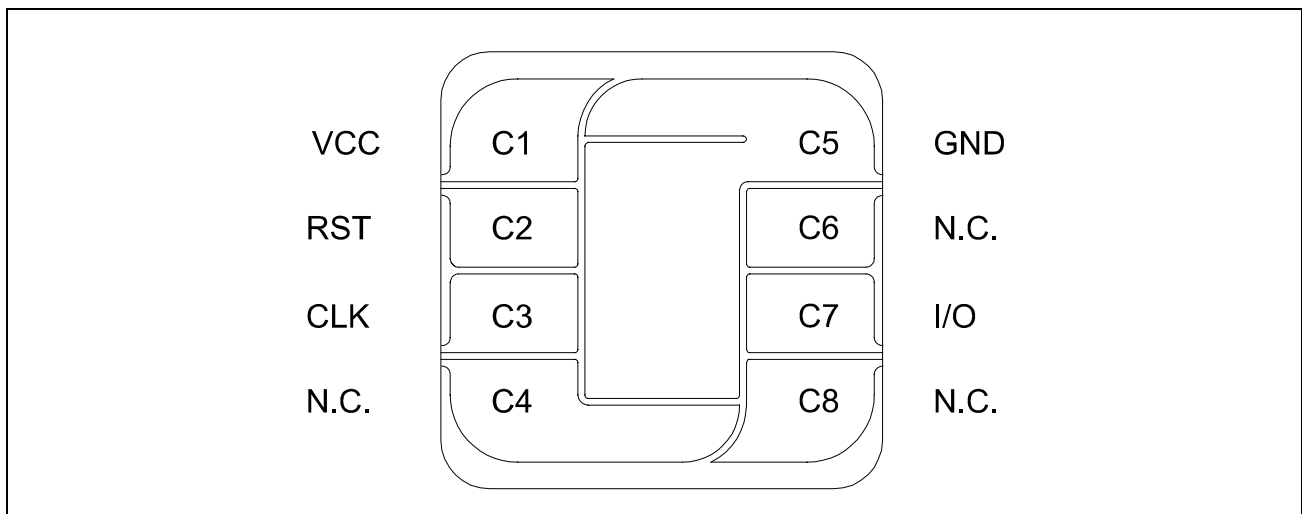
Pin Description

VCC — C1   C5 — GND

RST — C2   C6 — N.C.

CLK — C3   C7 — I/O

N.C. — C4   C8 — N.C.

Figure 1   Pin Configuration (top view)

Pin Definitions and Functions

Card Contact	Symbol	Function
C1	VCC	Operating voltage
C2	RST	Reset input
C3	CLK	Processor clock input
C5	GND	Ground
C4; C6; C8	N.C.	Not connected
C7	I/O	Bi-directional data port

General Description

SLE 66CX80S is a member of the Infineon Technologies high end security controller family in 0.6 µm CMOS technology. The CPU provides the high efficiency of the SAB 8051 instruction set extended by additional powerful instructions together with enhanced performance, memory sizes and security features.

The cryptocontroller IC offers 31.5 Kbytes of User-ROM, 256 bytes internal RAM, 1 Kbyte XRAM and 8 Kbytes EEPROM. It suits the requirements of the new generation of operating systems.

The Advanced Crypto Engine is equipped with its own RAM of 700 bytes and supports all of today known public-key algorithms based on large integer modular arithmetic. It allows fast and efficient calculation of e.g. RSA operations with key lengths up to 2048 bit.

The random number generator (RNG) is able to supply the CPU with true random numbers on all conditions. The CRC module allows the easy generation of checksums according to ISO 3309 (16-Bit-CRC). The timer makes it easy to implement advanced communication protocols such as T=1 and all other time critical processes. An additional interrupt capability of the I/O module allows parallel operation of chip card and terminal. To minimize the overall power consumption, the chip card controller IC offers a sleep mode.

As an important measure, the chip provides a new and enhanced level of on-chip security features.

In conclusion, the SLE 66CX80S fulfills the requirements of all chip card applications, as especially information security, access control, electronic banking and health care. The SLE 66CX80S is a powerful chip card cryptocontroller IC integrating outstanding memory sizes, an extended crypto-coprocessor, additional peripherals in combination with enhanced performance and optimized power consumption on an minimized die size. Therefore, the SLE 66CX80S offers the basis for new chip card applications.