

HZU5.6Z

Silicon Epitaxial Planar Zener Diode for Surge Absorb

HITACHI

ADE-208-795(Z)

Rev 0

May. 1999

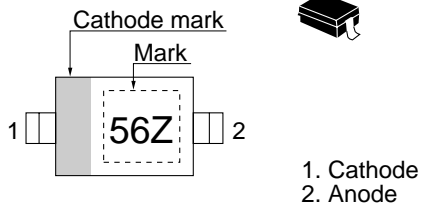
Features

- Low capacitance ($C=8.5\text{pF}$ max) and can protect ESD of signal line.
- Ultra small Resin Package (URP) is suitable for surface mount design.

Ordering Information

Type No.	Laser Mark	Package Code
HZU5.6Z	56Z	URP

Outline



Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Value	Unit
Power dissipation	Pd ¹	200	mW
Junction temperature	Tj	150	°C
Storage temperature	Tstg	-55 to +150	°C

Note 1. See Fig.2.

Electrical Characteristics (Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Zener voltage	V _Z	5.31	—	5.92	V	I _Z = 5 mA, 40ms pulse
Reverse current	I _R	—	—	0.5	μA	V _R = 2.5V
Capacitance	C	—	8.0	8.5	pF	V _R = 0V, f = 1 MHz
Dynamic resistance	r _d	—	—	80	Ω	I _Z = 5 mA
ESD-Capability ²	—	8	—	—	kV	C = 150pF, R = 330 Ω, Both forward and reverse direction 10 pulse

Notes 1. Failure criterion ; IR > 0.5 μA at VR = 2.5V.

Main Characteristic

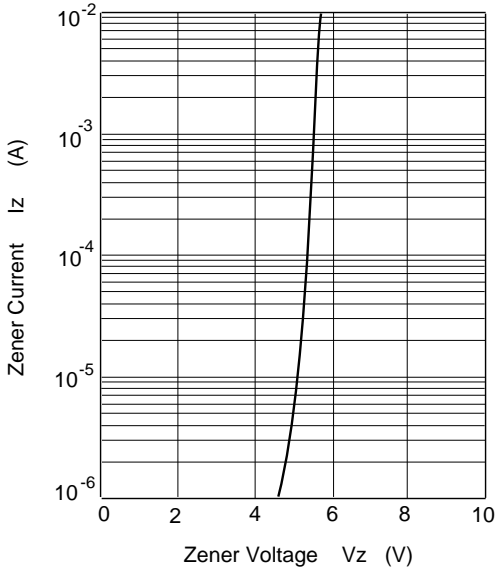


Fig.1 Zener current Vs. Zener voltage

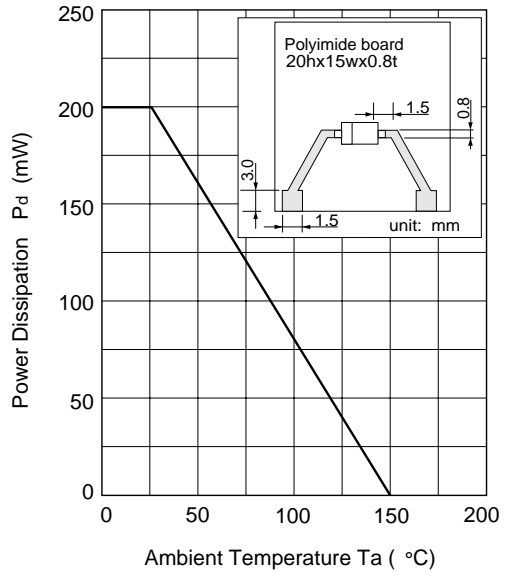


Fig.2 Power Dissipation Vs. Ambient Temperature

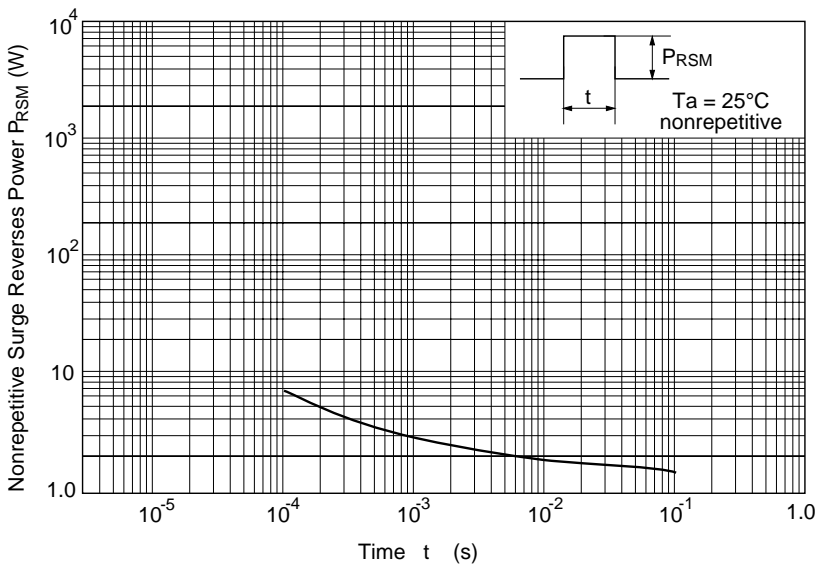


Fig.3 Surge Reverse Power Ratings

Main Characteristic

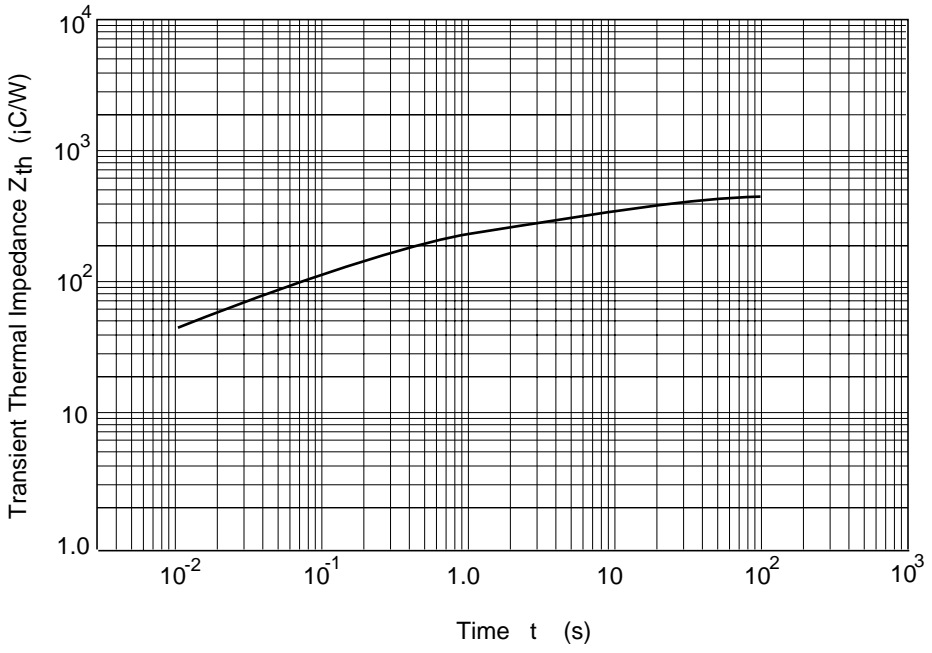
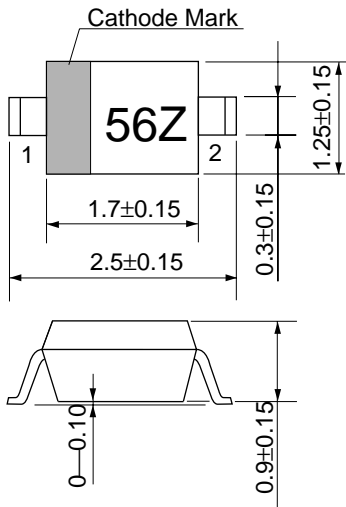


Fig.4 Transient Thermal Impedance

Package Dimensions

Unit : mm



- 1. Cathode
- 2. Anode

Hitachi Code	URP
JEDECCode	—
EIAJCode	—
Weight(g)	0.004

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