

MC12015 MC12016 MC12017

Dual Modulus Prescaler

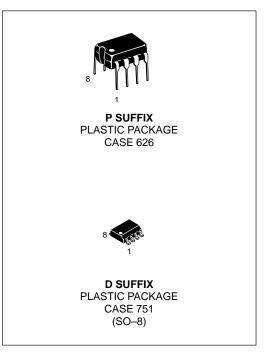
The MC12015, MC12016 and MC12017 are dual modulus prescalers which will drive divide by 32 and 33, 40 and 41, and 64 and 65, respectively. An internal regulator is provided to allow these devices to be used over a wide range of power–supply voltages. The devices may be operated by applying a supply voltage of 5.0 Vdc $\pm 10\%$ at Pin 7, or by applying an unregulated voltage source from 5.5Vdc to 9.5 Vdc to Pin 8.

- 225 MHz Toggle Frequency
- Low-Power 7.5 mA Maximum at 6.8 V
- Control Input and Output Are Compatible With Standard CMOS
- Connecting Pins 2 and 3 Allows Driving One TTL Load
- Supply Voltage 4.5 V to 9.5 V

SIMPLIFIED BLOCK DIAGRAM Control Input TO Vreg $0.001 \mu F$ Active Pullup ÷N / N+1 Signal Output ĞND $0.001 \mu F$ V_{reg} $0.1 \mu F$ VCC Voltage Regulator $0.1 \mu F$ 1. V_{reg} at Pin 7 is not guaranteed to be between 4.5 and 5.5V when V_{CC} is being applied to Pin 8 2. Pin 7 is not to be used as a source of regulated output voltage

MECL PLL COMPONENTS DUAL MODULUS PRESCALER

SEMICONDUCTOR TECHNICAL DATA



ORDERING INFORMATION

Device	Operating Temperature Range	Package
MC12015D		
MC12016D	T 400 to 1050C	SO-8
MC12017D		
MC12015P	$T_A = -40^{\circ} \text{ to } +85^{\circ}\text{C}$	
MC12016P		Plastic
MC12017P		

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MAXIMUM RATINGS [tblhead]

Rating	Symbol	Value	Unit
Regulated Voltage, Pin 7	V _{reg}	8.0	Vdc
Power Supply Voltage, Pin 8	Vcc	10	Vdc
Operating Temperature Range	TA	-40 to +85	°C
Storage Temperature Range	T _{stg}	-65 to +175	°C

NOTE: ESD data available upon request.

ELECTRICAL CHARACTERISTICS ($V_{CC} = 5.5 \text{ to } 9.5 \text{ V}; V_{reg} = 4.5 \text{ to } 5.5 \text{ V}; T_A = -40 \text{ to } 85^{\circ}\text{C}, \text{ unless otherwise noted.}$)

Characteristic	Symbol	Min	Тур	Max	Unit
Toggle Frequency (Sine Wave Input)					MHz
	f _{max}	225	_	-	
4U.com	fmin	-	-	35	
Supply Current	ICC	-	6.0	7.8	mA
Control Input HIGH (÷32, 40 or 64)	ViH	2.0	_	-	V
Control Input LOW (÷33, 41 or 65)	VIL	_	_	0.8	V
Output Voltage HIGH (I _{SOUrce} = 50μA) [Nofe 1]	Voн	2.5	_	_	V
Output Voltage LOW (I _{sink} = 2mA) [Note 1]	VOL	_	-	0.5	V
Input Voltage Sensitivity	V _{in}				mVpp
35 MHz		400	_	800	
50 to 225 MHz		200	-	800	
PLL Response Time [Notes 2 and 3]	tPLL	_	_	t _{out} to 70	ns

NOTES: 1. Pin 2 connected to Pin 3.

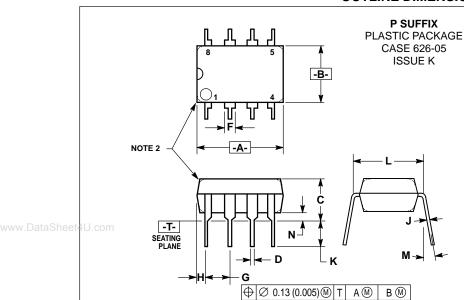
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^{2.} tp_L = the period of time the PLL has from the prescaler rising output tranistion (50%) to the modulus control input edge transition (50%) to ensure proper modulus selection.

3. t_{out} = period of output waveform.

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OUTLINE DIMENSIONS



NOTES:

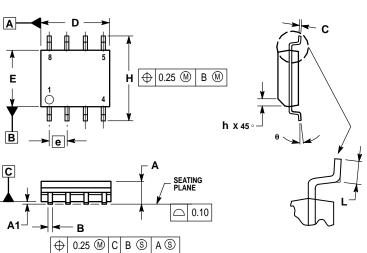
- DIMENSION L TO CENTER OF LEAD WHEN FORMED PARALLEL.
- FUNKHEU PAKALLEL.

 2. PACKAGE CONTOUR OPTIONAL (ROUND OR SOUARE CORNERS).

 3. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

	MILLIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
Α	9.40	10.16	0.370	0.400
В	6.10	6.60	0.240	0.260
С	3.94	4.45	0.155	0.175
D	0.38	0.51	0.015	0.020
F	1.02	1.78	0.040	0.070
G	2.54	BSC	0.100	BSC
Н	0.76	1.27	0.030	0.050
J	0.20	0.30	0.008	0.012
K	2.92	3.43	0.115	0.135
L	7.62 BSC		0.300 BSC	
M	_	10°	_	10°
N	0.76	1.01	0.030	0.040

D SUFFIX PLASTIC PACKAGE CASE 751-06 (SO-8) ISSUE T



- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
 2. DIMENSIONS ARE IN MILLIMETER.
 3. DIMENSION D AND E DO NOT INCLUDE MOLD PROTRUSION.
 4. MAXIMUM MOLD PROTRUSION 0.15 PER SIDE.
 - DIMENSION B DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR
 - PROTRUSION SHALL BE 0.127 TOTAL IN EXCESS OF THE B DIMENSION AT MAXIMUM MATERIAL CONDITION.

	MILLIMETERS		
DIM	MIN	MAX	
Α	1.35	1.75	
A1	0.10	0.25	
В	0.35	0.49	
С	0.19	0.25	
D	4.80	5.00	
Е	3.80	4.00	
е	1.27 BSC		
Н	5.80	6.20	
h	0.25	0.50	
L	0.40	1.25	
Δ.	N٥	7∘	

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