

CMKD6001

**SURFACE MOUNT
TRIPLE ISOLATED
LOW LEAKAGE SILICON
SWITCHING DIODES**



www.centrasemi.com

DESCRIPTION:

The CENTRAL SEMICONDUCTOR CMKD6001 type contains three (3) Isolated Silicon Switching Diodes, manufactured by the epitaxial planar process, epoxy molded in a ULTRAmimi™ surface mount package, designed for switching applications requiring extremely low leakage.

MARKING CODE: K01

ULTRAmimi™



SOT-363 CASE

MAXIMUM RATINGS: ($T_A=25^\circ\text{C}$)

Continuous Reverse Voltage
Peak Repetitive Reverse Voltage
Continuous Forward Current
Peak Repetitive Forward Current
Peak Forward Surge Current, $t_p=1.0\mu\text{s}$
Peak Forward Surge Current, $t_p=1.0\text{s}$
Power Dissipation
Operating and Storage Junction Temperature
Thermal Resistance

SYMBOL

V_R	75
V_{RRM}	100
I_F	250
I_{FRM}	500
I_{FSM}	4.0
I_{FSM}	1.0
P_D	250
T_J, T_{stg}	-65 to +150
θ_{JA}	500

UNITS

V
V
mA
mA
A
A
mW
$^\circ\text{C}$
$^\circ\text{C/W}$

ELECTRICAL CHARACTERISTICS PER DIODE: ($T_A=25^\circ\text{C}$ unless otherwise noted)

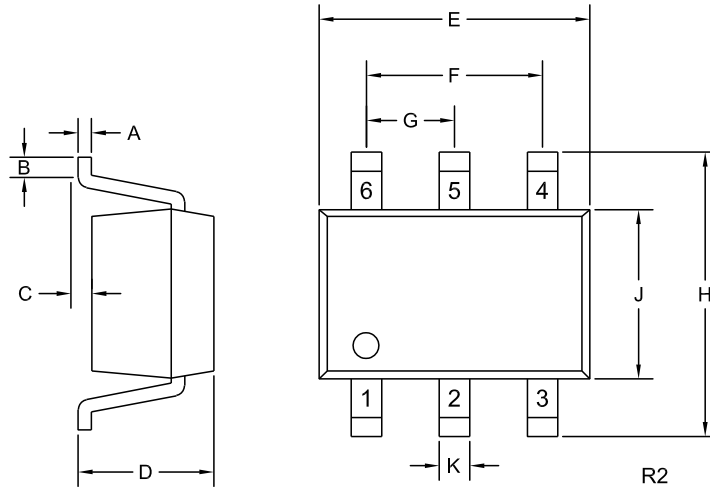
SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
I_R	$V_R=75\text{V}$		500	pA
BV_R	$I_R=100\mu\text{A}$	100		V
V_F	$I_F=1.0\text{mA}$		0.85	V
V_F	$I_F=10\text{mA}$		0.95	V
V_F	$I_F=100\text{mA}$		1.1	V
C_T	$V_R=0, f=1.0\text{MHz}$		2.0	pF
t_{rr}	$I_R=I_F=10\text{mA}, R_L=100\Omega$ Rec. to 1.0mA		3.0	μs

CMKD6001

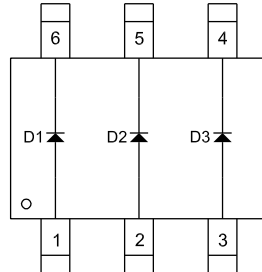
**SURFACE MOUNT
TRIPLE ISOLATED
LOW LEAKAGE SILICON
SWITCHING DIODES**



SOT-363 CASE - MECHANICAL OUTLINE



PIN CONFIGURATION



LEAD CODE:

- 1) Anode D1
- 2) Anode D2
- 3) Anode D3
- 4) Cathode D3
- 5) Cathode D2
- 6) Cathode D1

MARKING CODE: K01

SYMBOL	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.004	0.010	0.10	0.25
B	0.005	-	0.12	-
C	0.000	0.004	0.00	0.10
D	0.031	0.043	0.80	1.10
E	0.071	0.087	1.80	2.20
F	0.051		1.30	
G	0.026		0.65	
H	0.075	0.091	1.90	2.30
J	0.043	0.055	1.10	1.40
K	0.006	0.012	0.15	0.30

SOT-363 (REV: R2)

R4 (13-January 2010)