

# LM2904WH

## Low power dual operational amplifier

### Features

- Internally frequency compensated
- Large DC voltage gain: 100 dB
- Wide bandwidth (unity gain: 1.1 MHz temperature compensated)
- Very low supply current per operator (500 µA)
- Low input bias current: 20 nA (temperaturecompensated)
- Low input offset current: 2 nA
- Input common-mode voltage range includes ground
- Differential input voltage range equal to the power supply voltage
- Large output voltage swing 0 V to V<sub>CC</sub> 1.5 V
- Internal ESD protection:
  - 2 kV HBM
  - 200 V MM

### Description

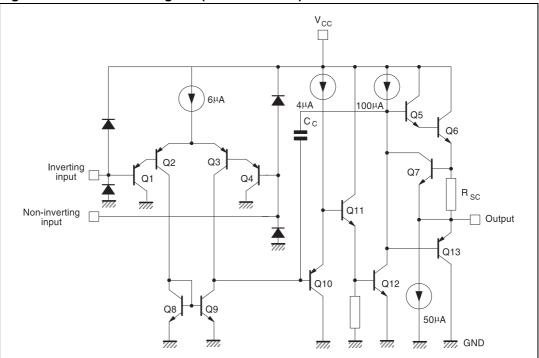
This circuit consists of two independent, highgain, internally frequency-compensated operational amplifiers, designed specifically for automotive and industrial control systems. It operates from a single power supply over a wide range of voltages. The low power supply drain is independent of the magnitude of the power supply voltage.

Application areas include transducer amplifiers, DC gain blocks and all the conventional op-amp circuits which now can be more easily implemented in single power supply systems. For example, these circuits can be directly supplied from standard +5 V which is used in logic systems and will easily provide the required interface electronics without requiring any additional power supply.



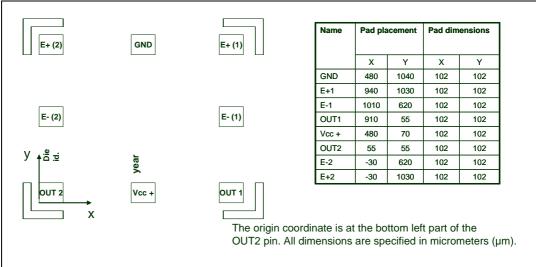
In linear mode, the input common-mode voltage range includes ground and the output voltage can also swing to ground, even though operated from a single power supply.

## 1 Schematic diagram



### Figure 1. Schematic diagram (1/2 LM2904WH)







## 2 Absolute maximum ratings and operating conditions

Symbol	Parameter	Value	Unit
-	i arameter	Value	onne
$V_{CC}^+$	Supply voltage	+32	V
V <sub>id</sub>	Differential input voltage	-0.3 to V <sub>CC</sub> +0.3	V
V <sub>in</sub>	Input voltage	-0.3 to V <sub>CC</sub> +0.3	V
	Output short-circuit to ground <sup>(1)</sup>	40	mA
Тj	Maximum junction temperature	150	°C
R <sub>thja</sub>	Thermal resistance junction to ambient <sup>(2)</sup> SO-8	125	°C/W
R <sub>thjc</sub>	Thermal resistance junction to case <sup>(2)</sup> SO-8	40	°C/W
l <sub>in</sub>	Input current <sup>(3)</sup>	5	mA
T <sub>stg</sub>	Storage temperature range	-65 to +150	°C
	HBM: human body model <sup>(4)</sup>	2	kV
ESD	MM: machine model <sup>(5)</sup>	200	V
	CDM: charged device model <sup>(6)</sup>	1.5	kV

#### Table 1. Absolute maximum ratings

 Short-circuits from the output to V<sub>CC</sub> can cause excessive heating if V<sub>CC</sub><sup>+</sup> > 15 V. The maximum output current is approximately 40 mA, independent of the magnitude of V<sub>CC</sub>. Destructive dissipation can result from simultaneous short-circuits on all amplifiers.

2. Short-circuits can cause excessive heating and destructive dissipation. Values are typical.

- 3. This input current only exists when the voltage values applied on the inputs is beyond the supply voltage line limits. This is not destructive if the current does not exceed 5 mA as indicated, and normal output is restored for input voltages above -0.3V.
- Human body model: A 100pF capacitor is charged to the specified voltage, then discharged through a 1.5kΩ resistor between two pins of the device. This is done for all couples of connected pin combinations while the other pins are floating.
- 5. Machine model: A 200pF capacitor is charged to the specified voltage, then discharged directly between two pins of the device with no external series resistor (internal resistor < 5Ω). This is done for all couples of connected pin combinations while the other pins are floating.
- 6. Charged device model: all pins and the package are charged together to the specified voltage and then discharged directly to the ground through only one pin. This is done for all pins.

Table 2.Operating conditions

Symbol	Parameter	Value	Unit
V <sub>CC</sub> <sup>+</sup>	Supply voltage	3 to 30	V
T <sub>oper</sub>	Operating free-air temperature range	-40 to +150	°C
V <sub>icm</sub>	Input common mode voltage range $(V_{CC}^+=+30V)^{(1)}$ $T_{amb} = 25^{\circ}C$ $T_{min} \leq T_{amb} \leq T_{max}$	0 to V <sub>CC</sub> <sup>+</sup> -1.5 0 to V <sub>CC</sub> <sup>+</sup> -2	V

1. The input common-mode voltage of either input signal voltage should not be allowed to go negative by more than 0.3 V. The upper end of the common-mode voltage range is  $V_{CC}^+$ -1.5 V, but either or both inputs can go to +32 V without damage.



# 3 Electrical characteristics

Table 3.  $V_{CC}^+ = 5V, V_{CC}^- =$ Ground,  $V_o = 1.4V, T_{amb} = 25^{\circ}C$  (unless otherwise specified)

Symbol	Parameter	Min.	Тур.	Max.	Unit
V <sub>io</sub>	Input offset voltage <sup>(1)</sup> T <sub>min</sub> ≤ T <sub>amb</sub> ≤ T <sub>max</sub>		2	7 9	mV
l <sub>io</sub>	Input offset current $T_{min} \le T_{amb} \le T_{max}$		2	30 40	nA
l <sub>ib</sub>	Input bias current <sup>(2)</sup> $T_{min} \le T_{amb} \le T_{max}$		20	150 200	nA
A <sub>vd</sub>	Large signal voltage gain $V_{CC}^+$ = +15V,R <sub>L</sub> =2k $\Omega$ , V <sub>o</sub> = 1.4V to 11.4V $T_{min} \leq T_{amb} \leq T_{max}$	50 2.5	100		V/mV
SVR	Supply voltage rejection ratio $V_{CC}^+$ = +5 to +30V, R <sub>S</sub> ≤ 10kΩ $T_{min} \le T_{amb} \le T_{max}$	65 65	100		dB
I <sub>CC</sub>	Supply current, all amps, no load $V_{CC}^+ = +5V$ $T_{min} \le T_{amb} \le T_{max}$ , $V_{CC} = +30V$		0.7	1.2 2	mA
CMR	Common-mode rejection ratio ( $R_S = 10k\Omega$ ) $T_{min} \le T_{amb} \le T_{max}$	70 60	85		dB
I <sub>source</sub>	Output short-circuit current $V_{CC}^+$ = +15V, $V_o$ = +2V, $V_{id}$ = +1V $T_{min} \le T_{amb} \le T_{max}$	20 10	40	60	mA
I <sub>sink</sub>	Output sink current $V_O = 2V, V_{CC}^+ = +5V$ $T_{min} \le T_{amb} \le T_{max}$	10 5	20		mA
Unit	$V_{O}$ = +0.2V, $V_{CC}^{+}$ = +15V $T_{min} \le T_{amb} \le T_{max}$	12 10	50		μA
V <sub>OPP</sub>	Output voltage swing ( $R_L = 2k\Omega$ ) $T_{min} \le T_{amb} \le T_{max}$	0 0		V <sub>CC</sub> <sup>+</sup> -1.5 V <sub>CC</sub> <sup>+</sup> -2	V
V <sub>OH</sub>	High level output voltage (V <sub>CC</sub> <sup>+</sup> = + 30V) $R_L = 2k\Omega$ $T_{min} \le T_{amb} \le T_{max}$ $R_L = 10k\Omega$ $T_{min} \le T_{amb} \le T_{max}$	26 26 27	27 28		V
V <sub>OL</sub>	$\begin{aligned} & \text{Tmin} \leq \text{Tamb} \leq \text{Tmax} \\ & \text{Low level output voltage (RL = 10kΩ)} \\ & \text{T}_{min} \leq \text{T}_{amb} \leq \text{T}_{max} \end{aligned}$	27	5	20 20	mV

specified) (continued)					
Symbol	Parameter	Min.	Тур.	Max.	Unit
SR	Slew rate (unity gain) $V_{CC}^+ = 15V$ , Vi = 0.5 to 3V, $R_L = 2k\Omega$ , $C_L = 100pF$ , $T_{min} \le T_{amb} \le T_{max}$	0.3 0.2	0.6		V/µs
GBP	Gain bandwidth product f = 100kHz $V_{CC}^+$ = 30V, $V_{in}$ = 10mV, $R_L$ = 2k $\Omega$ , $C_L$ = 100pF $T_{min} \leq T_{amb} \leq T_{max}$	0.7 0.45	1.1		MHz
THD	Total harmonic distortion f = 1kHz, $A_V$ = 20dB, $R_L$ = 2k $\Omega$ , $V_o$ = 2 $V_{pp}$ , $C_L$ = 100pF, $V_{CC}$ = 30V		0.02		%
e <sub>n</sub>	Equivalent input noise voltage f = 1kHz, R <sub>S</sub> = 100Ω V <sub>CC</sub> = 30V		55		nV/√Hz
DVio	Input offset voltage drift		7	30	µV/°C
DI <sub>io</sub>	Input offset current drift		10	300	pA/°C
V <sub>O1</sub> /V <sub>O2</sub>	Channel separation $^{(3)}$ 1kHz $\leq$ f $\leq$ 20kHz		120		dB

# Table 3. $V_{CC}^+ = 5V, V_{CC}^- =$ Ground, $V_o = 1.4V, T_{amb} = 25^{\circ}C$ (unless otherwise specified) (continued)

1.  $V_{O} = 1.4 \text{ V}, \text{ R}_{S} = 0 \Omega, 5 \text{ V} < V_{CC}^{+} < 30 \text{ V}, 0 \text{ V} < V_{ic} < V_{CC}^{+} - 1.5 \text{ V}.$ 

2. The direction of the input current is out of the IC. This current is essentially constant, independent of the state of the output, so there is no change in the loading charge on the input lines.

3. Due to the proximity of external components, ensure that stray capacitance does not cause coupling between these external parts. Typically, this can be detected because this type of capacitance increases at higher frequencies.

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**Open loop frequency response** 

Figure 3.

Large signal frequency response

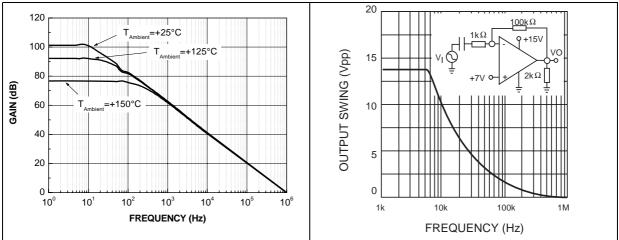
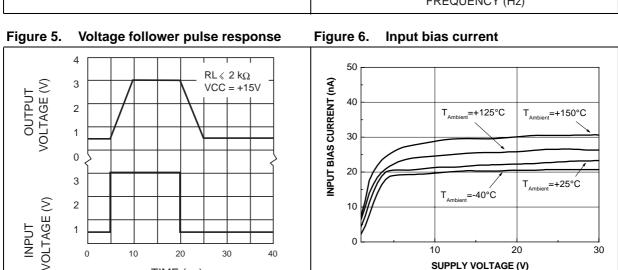
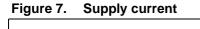


Figure 4.





0

10

20

TIME (µs)

30

40

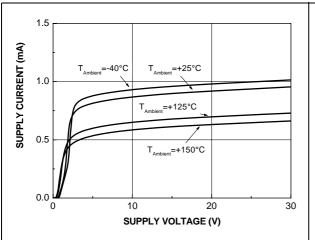
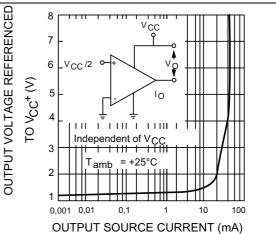


Figure 8. **Output characteristics** 

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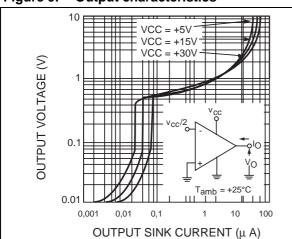


SUPPLY VOLTAGE (V)

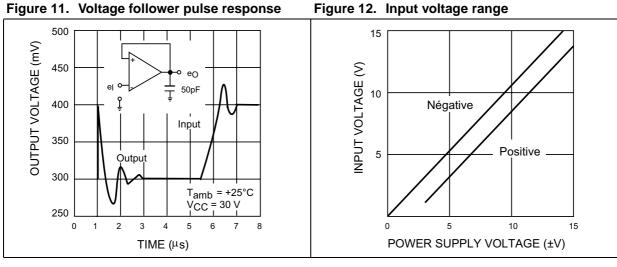
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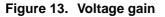
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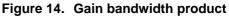
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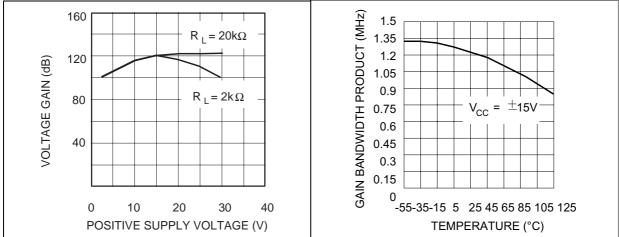
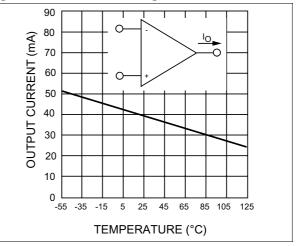
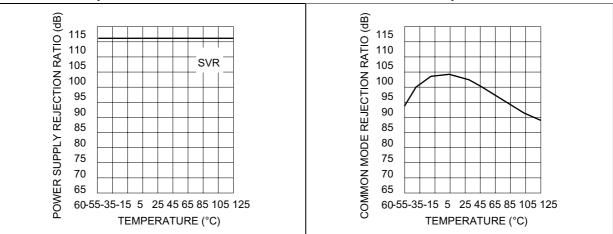


Figure 9. **Output characteristics**  Figure 10. Current limiting



# Figure 15. Power supply rejection ratio versus Figure 16. Common mode rejection ratio temperature versus temperature





## 4 Package information

In order to meet environmental requirements, STMicroelectronics offers these devices in ECOPACK<sup>®</sup> packages. These packages have a lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an STMicroelectronics trademark. ECOPACK specifications are available at: www.st.com.



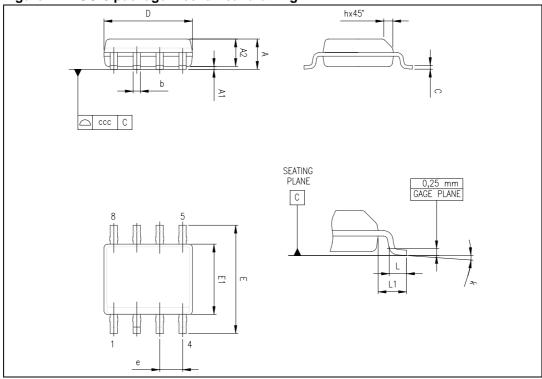


Figure 17. SO-8 package mechanical drawing

### Table 4.SO-8 package mechanical data

			Dime	nsions					
Ref.		Millimeters			Inches				
	Min.	Тур.	Max.	Min.	Тур.	Max.			
А			1.75			0.069			
A1	0.10		0.25	0.004		0.010			
A2	1.25			0.049					
b	0.28		0.48	0.011		0.019			
С	0.17		0.23	0.007		0.010			
D	4.80	4.90	5.00	0.189	0.193	0.197			
E	5.80	6.00	6.20	0.228	0.236	0.244			
E1	3.80	3.90	4.00	0.150	0.154	0.157			
е		1.27			0.050				
h	0.25		0.50	0.010		0.020			
L	0.40		1.27	0.016		0.050			
k	1°		8°	1°		8°			
CCC			0.10			0.004			



## 5 Ordering information

### Table 5. Order codes

Order code	Temperature range	Package	Packing	Marking
JLM2904WH-CD1		Wafer		
LM2904WHD LM2904WHDT	-40°C, +150°C	SO-8	Tube or Tape & reel	2904WH
LM2904WHYD <sup>(1)</sup> LM2904WHYDT <sup>(1)</sup>		SO-8 (Automotive grade)	Tube or Tape & reel	2904WHY

1. Qualified and characterized according to AEC Q100 and Q003 or equivalent, advanced screening according to AEC Q001 & Q 002 or equivalent.

## 6 Revision history

Date	Revision	Changes
01-Sep-2003	1	Initial release.
01-Jul-2005	2	PPAP references inserted in the datasheet, see Section 5: Ordering information.
01-Oct-2005	3	Correction of error in A <sub>VD</sub> min. value in <i>Table 3</i> . Minor grammatical and formatting changes throughout.
27-Sep- 2006	4	Correction of error in A <sub>VD</sub> min. value in <i>Table 3</i> .
20-Jul-2007	5	ESD values added in <i>Table 1: Absolute maximum ratings</i> . Equivalent input noise parameter added in <i>Table 3</i> . Electrical characteristics curves updated. <i>Section 4: Package information</i> updated.
07-Apr-2008	6	Added R <sub>thja</sub> and R <sub>thjc</sub> parameters in <i>Table 1: Absolute maximum ratings</i> . Updated format of package information for SO-8. Corrected marking error in <i>Table 5: Order codes</i> (2904WHY, not 2904WY).

### Table 6.Document revision history



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