# Power transistor (60V, 3A)

## 2SC5824

#### Features

- 1) High speed switching. (Tf : Typ. : 30ns at Ic = 3A)
- 2) Low saturation voltage, typically (Typ. : 200mV at Ic = 2A, IB = 200mA)
- 3) Strong discharge power for inductive load and capacitance load.
- 4) Complements the 2SA2071.

#### Applications

NPN Silicon epitaxial planar transistor

#### Structure

Low frequency amplifier High speed switching

#### Packaging specifications

	Package	Taping
Туре	Code	T100
	Basic ordering unit (pieces)	1000
2SC5824		0

#### •Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit	
Collector-base voltage	Vсво	60	V	
Collector-emitter voltage	Vceo	60	V	
Emitter-base voltage	Vebo	6	V	
Collector current	lc	3	A	
	Іср	6	A *1	
Power dissipation	Pc	500	mW *2	
	Pc	2.0	W *3	
Junction temperature	Tj	150	°C	
Range of storage temperature	Tstg	-55~+150	°C	

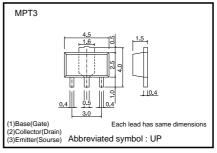
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\*1 Pw=100ms

\*2 Each terminal mounted on a recommended land.

\*3 Mounted on a 40x40x0.7(mm) ceramic substrate

#### •Dimensions (Unit : mm)



### Transistor

#### Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	ВУсво	60	-	_	V	Ic=100μA
Collector-emitter breakdown voltage	BVceo	60	-	-	V	Ic=1mA
Emitter-base breakdown voltage	ВVево	6	-	-	V	Ιε=100μΑ
Collector cut-off current	Ісво	-	-	1.0	μΑ	Vcb=40V
Emitter cut-off current	Іево	-	_	1.0	μΑ	VEB=4V
Collector-emitter staturation voltage	VCE(sat)	-	200	500	mV	Ic=2A, IB=200mA *1
DC current gain	hfe	120	-	390	-	Vce=2V, Ic=100mA
Transition frequency	fт	-	200	-	MHz	Vce=10V, Ie= -100mA, f=10MHz *1
Collector output capacitance	Cob	-	20	-	pF	Vcb=10V, IE=0mA, f=1MHz
Turn-on time	ton	-	50	-	ns	Ic=3A,
Storage time	tstg	_	150	_	ns	IB1=300mA IB2= −300mA Vcc≑25V <sup>*2</sup>
Fall time	tf	_	30	-	ns	

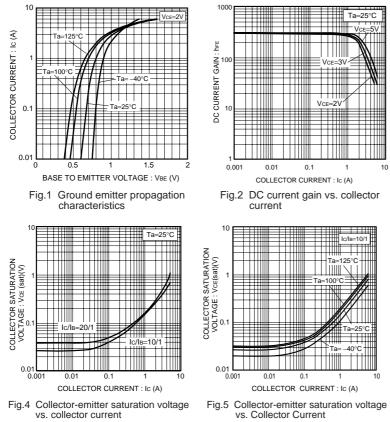
\*1 Non repetitive pulse

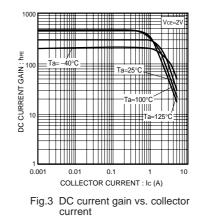
\*2 See switching charactaristics measurement circuits

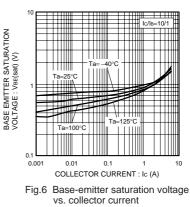
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Q	R		
120-270	180-390		

#### •Electrical characteristic curves







Rev.B

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## 2SC5824

## Transistor

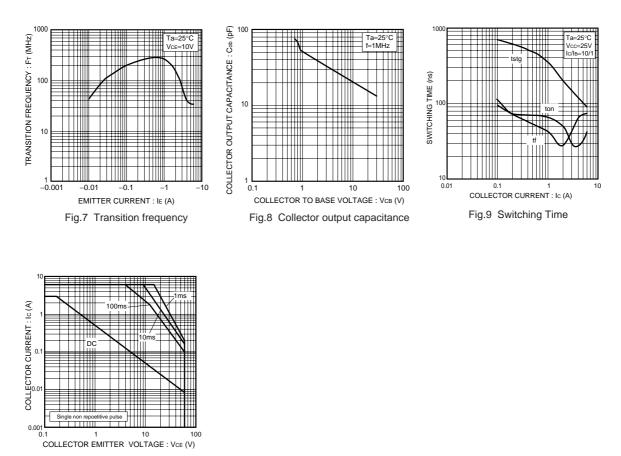
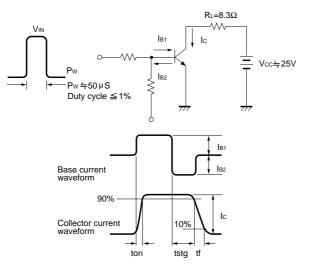


Fig.10 Safe operating area

#### •Switching characteristics measurement circuits



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Appendix1-Rev2.0

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