

Silicon PNP Power Transistors

2SA1567

DESCRIPTION

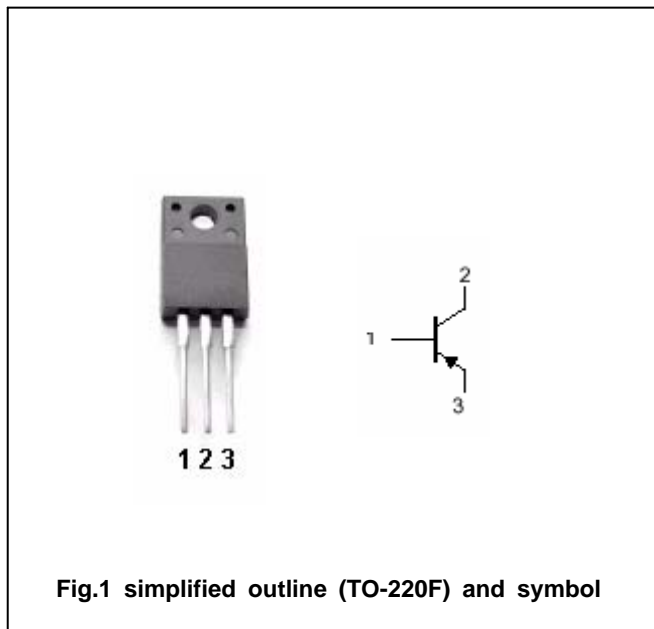
- With TO-220F package
- Complement to type 2SC4064
- Low collector-emitter saturation voltage

APPLICATIONS

- For DC motor driver ,chopper regulator and general purpose applications

PINNING

PIN	DESCRIPTION
1	Base
2	Collector
3	Emitter



Absolute maximum ratings(Ta=25)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{CBO}	Collector-base voltage	Open emitter	-50	V
V _{CEO}	Collector-emitter voltage	Open base	-50	V
V _{EBO}	Emitter-base voltage	Open collector	-6	V
I _C	Collector current		-12	A
I _B	Base current		-3	A
P _C	Collector power dissipation	T _C =25	35	W
T _j	Junction temperature		150	
T _{stg}	Storage temperature		-55~150	

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CHARACTERISTICS

T_j=25 unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C =-25mA; I _B =0	-50			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =-6A; I _B =-0.3 A			-0.35	V
I _{CBO}	Collector cut-off current	V _{CB} =-50V; I _E =0			-100	μ A
I _{EBO}	Emitter cut-off current	V _{EB} =-6V; I _C =0			-100	μ A
h _{FE}	DC current gain	I _C =-6A ; V _{CE} =-1V	50			
f _T	Transition frequency	I _E =0.5A ; V _{CE} =-12V		40		MHz
C _{OB}	Output capacitance	I _E =0; V _{CB} =-10V; f=1MHz		330		pF

Switching times

t _{on}	Turn-on time	I _C =-6A; R _L =4 I _{B1} =-I _{B2} =-0.12A V _{CC} =-24V		0.40		μ s
t _s	Storage time			0.40		μ s
t _f	Fall time			0.20		μ s

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PACKAGE OUTLINE

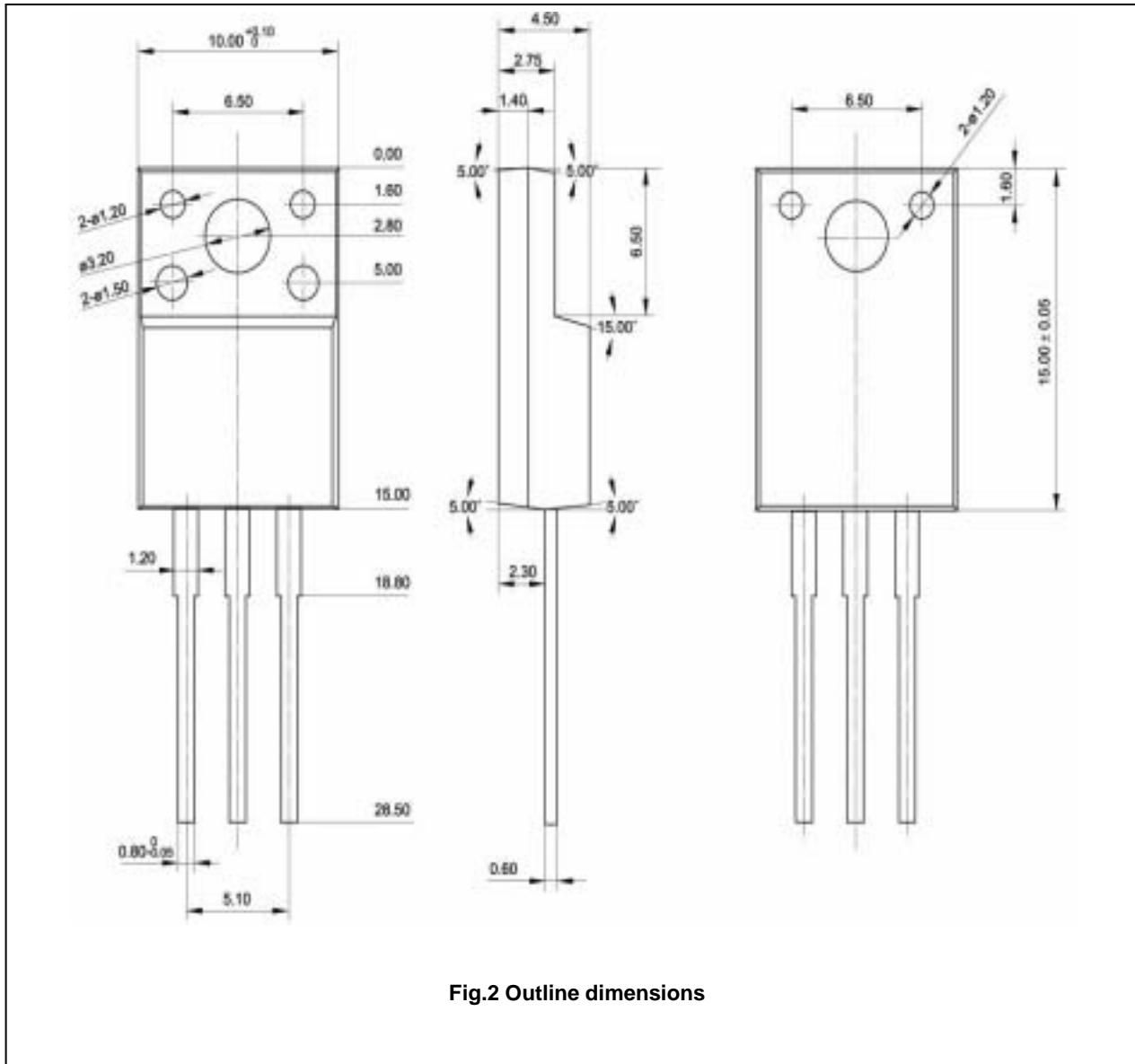


Fig.2 Outline dimensions

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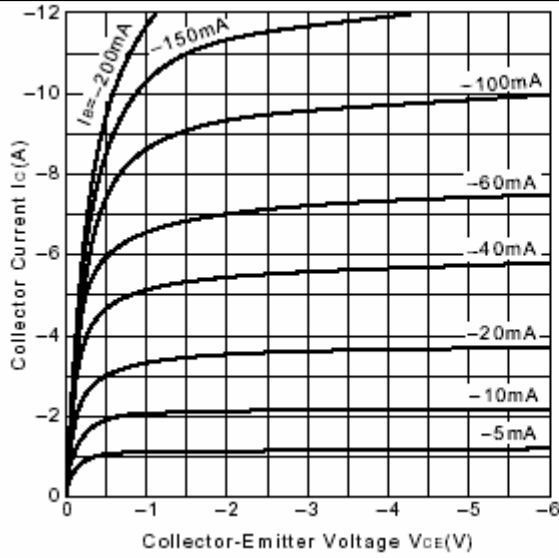


Fig.3 Static Characteristic

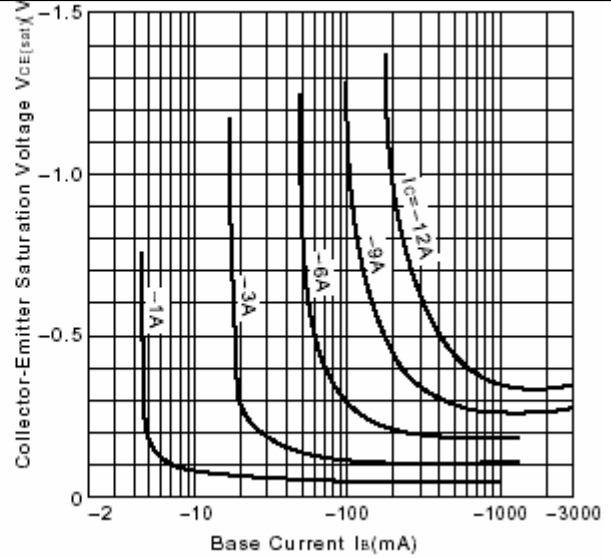


Fig.4 $V_{CE(sat)}-I_B$ Characteristics

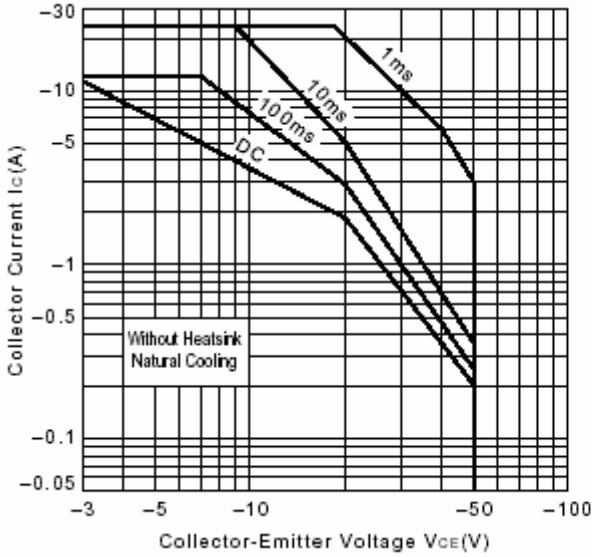


Fig.5 Safe Operating Area

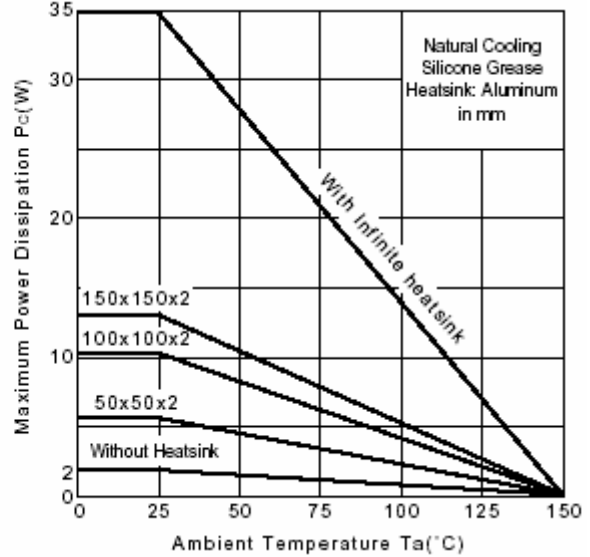


Fig.6 P_c-T_a Derating

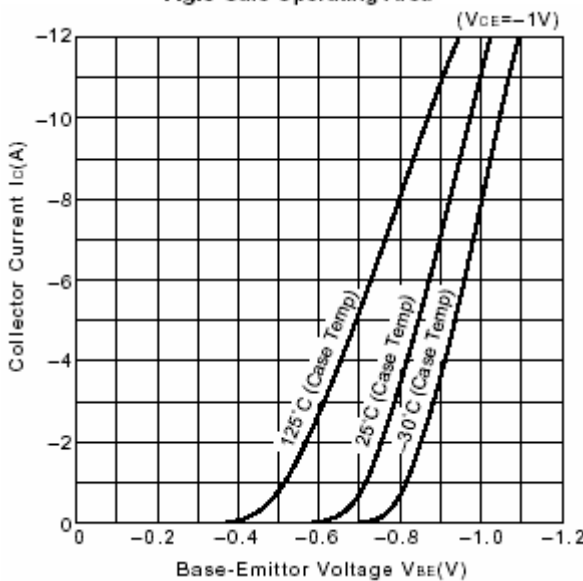


Fig.7 I_C-V_{BE}

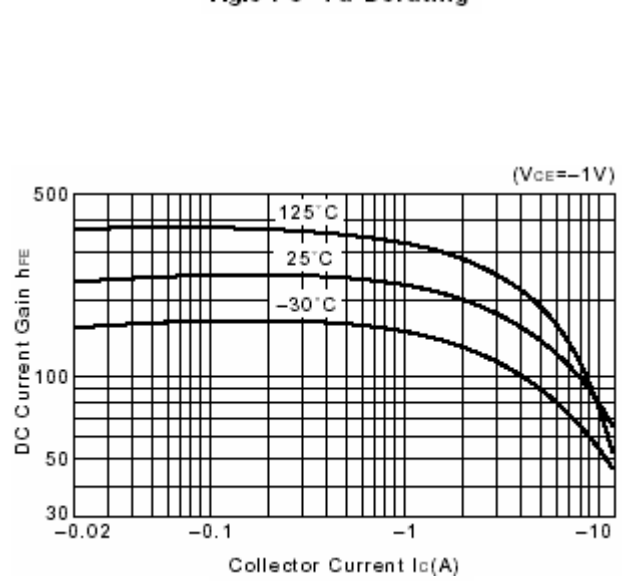


Fig.8 DC current Gain