

# 2SJ479(L), 2SJ479(S)

## Silicon P Channel MOS FET

REJ03G0866-0300 Rev.3.00 Jun 05, 2006

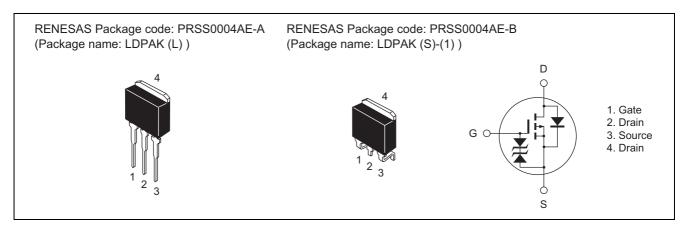
#### **Description**

High speed power switching

#### **Features**

- Low on-resistance  $R_{DS \; (on)} = 25 \; m\Omega \; typ. \label{eq:resistance}$
- 4 V gate drive devices.
- High speed switching

#### **Outline**



### **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Value	Unit
Drain to source voltage	V <sub>DSS</sub>	-30	V
Gate to source voltage	V <sub>GSS</sub>	±20	V
Drain current	I <sub>D</sub>	-30	A
Drain peak current	I <sub>D (pulse)</sub> Note 1	-120	A
Body to drain diode reverse drain current	I <sub>DR</sub>	-30	A
Channel dissipation	Pch Note 2	50	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1%

2. Value at  $Tc = 25^{\circ}C$ 

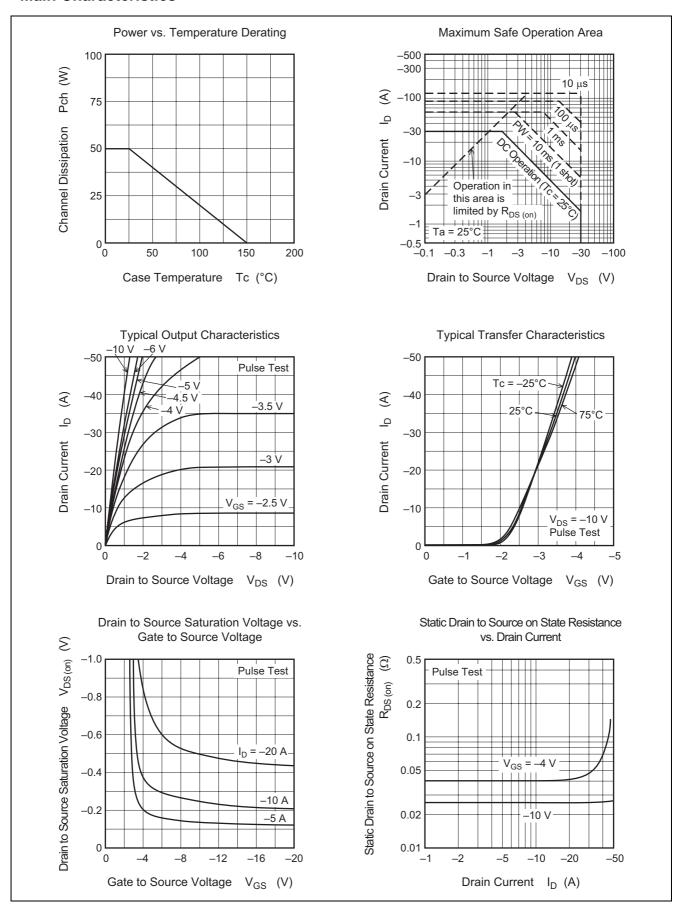
### **Electrical Characteristics**

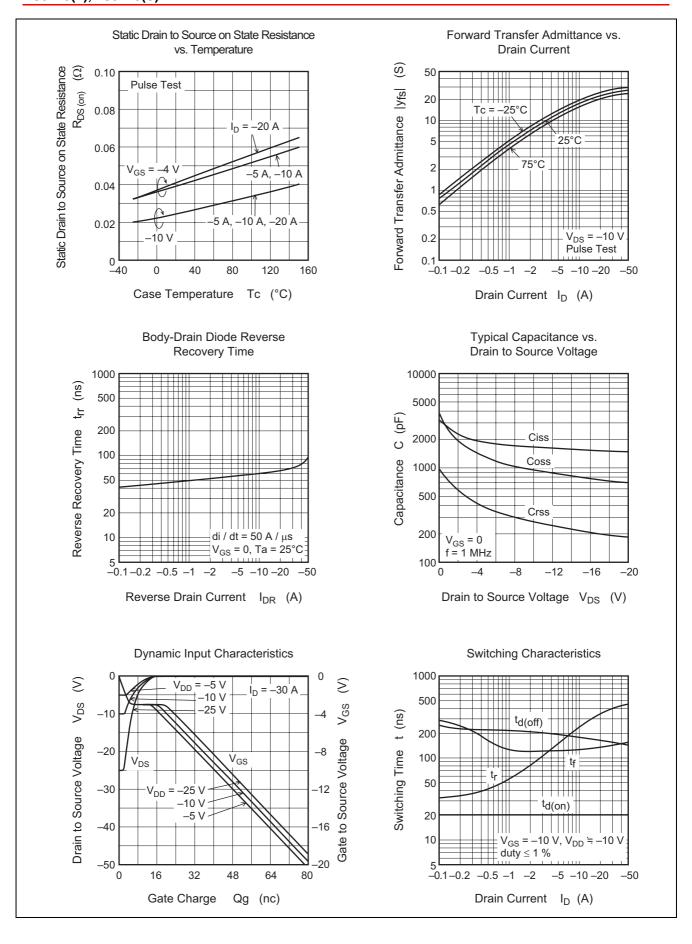
 $(Ta = 25^{\circ}C)$ 

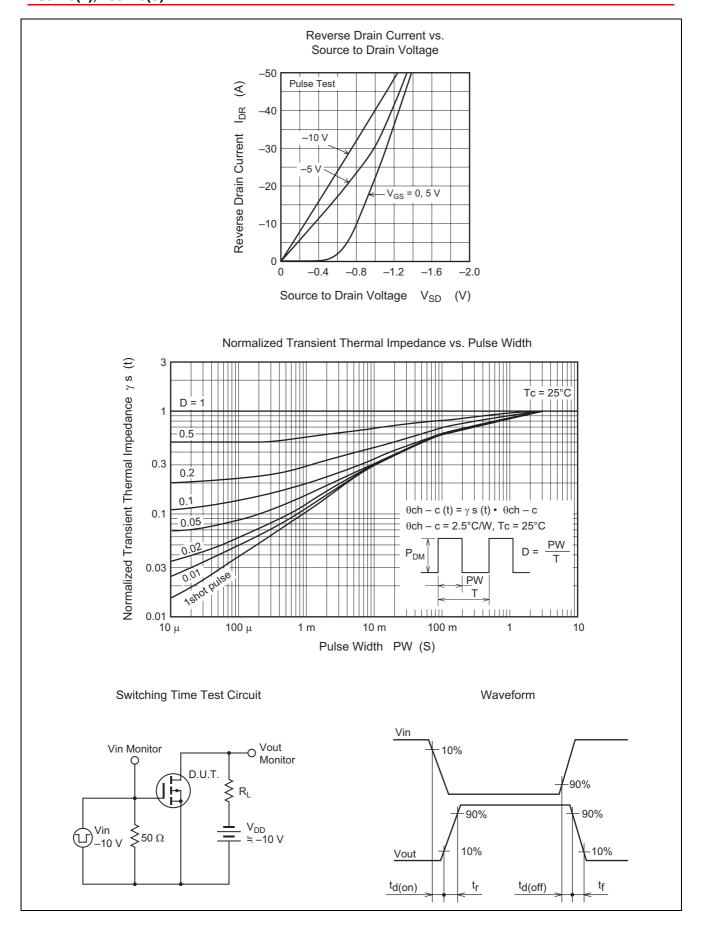
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V <sub>(BR) DSS</sub>	-30	_	_	V	$I_D = -10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	V <sub>(BR) GSS</sub>	±20	_	_	V	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	_	_	-10	μΑ	$V_{DS} = -30 \text{ V}, V_{GS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_	_	±10	μΑ	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
Gate to source cutoff voltage	V <sub>GS (off)</sub>	-1.0	_	-2.0	V	$I_D = -1 \text{ mA}, V_{DS} = -10 \text{ V}$
Static drain to source on state resistance	R <sub>DS (on)</sub>	_	25	35	mΩ	$I_D = -15 \text{ A}, V_{GS} = -10 \text{ V}^{\text{Note 3}}$
	R <sub>DS (on)</sub>	_	40	60	mΩ	$I_D = -15 \text{ A}, V_{GS} = -4 \text{ V}^{\text{Note 3}}$
Forward transfer admittance	y <sub>fs</sub>	12	20	_	S	$I_D = -15 \text{ A}, V_{DS} = -10 \text{ V}^{\text{Note 3}}$
Input capacitance	Ciss	_	1700	_	pF	$V_{DS} = -10 \text{ V}$
Output capacitance	Coss	_	950	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	260	_	pF	f = 1 MHz
Turn-on delay time	t <sub>d (on)</sub>	_	20	_	ns	V <sub>GS</sub> = -10 V
Rise time	t <sub>r</sub>	_	290	_	ns	$I_D = -15 \text{ A}$
Turn-off delay time	t <sub>d (off)</sub>	_	170	_	ns	$R_L = 0.67 \Omega$
Fall time	t <sub>f</sub>	_	130	_	ns	
Body to drain diode forward voltage	$V_{DF}$	_	-1.1	_	V	$I_F = -30 \text{ A}, V_{GS} = 0$
Body to drain diode reverse recovery time	t <sub>rr</sub>	_	70	_	ns	$I_F = -30 \text{ A}, V_{GS} = 0$
						di <sub>F</sub> /dt = 50 A/μs

Note: 3. Pulse test

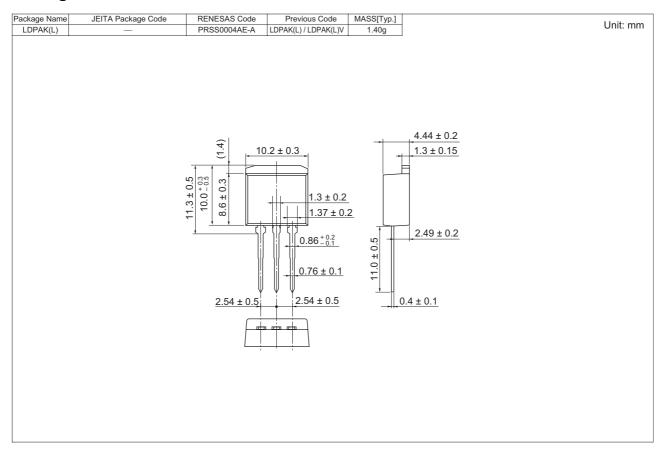
#### **Main Characteristics**

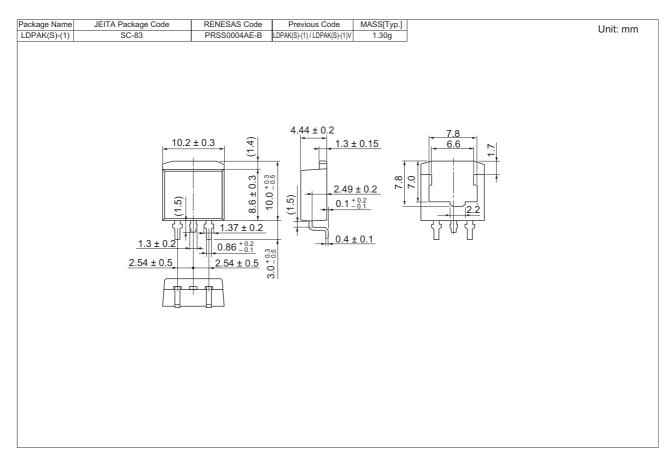






### **Package Dimensions**





### **Ordering Information**

Part Name	Quantity	Shipping Container		
2SJ479L-E	500 pcs	Box (Sack)		
2SJ479STL-E	1000 pcs	Taping		

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.

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