

## OLI 249 Radiation Tolerant Phototransistor Optocoupler For Hybrid Assembly





Color Code - BLACK

### SCHEMATIC

### Features

- Current transfer ratio guaranteed over -55°C to +100°C ambient temp. range
- 1500 Vdc electrical isolation
- Small foot print for hybrid device
- ♦ More Radiation tolerant than 4N49<sup>4</sup>
- High current transfer ratio at low input current - 200% at I<sub>F</sub> =2mA over temperature
- High reliability and rugged construction
- ♦ CTR comparable to darlington output but with low saturation V<sub>CE</sub> = 0.15v typ.
- Similar to 4N4X type optocouplers
- Custom package available Call Factory<sup>4</sup>

PACKAGE OUTLINE

## Description

The OLI 249 is designed especially for hybrid application requiring optical isolation with high current transfer ratio and low saturation Vce. Each OLI 249 consists of a light emitting diode and a NPN silicon phototransistor mounted and coupled in a miniature custom ceramic package. The very low input current makes the OLI 249 well suited for direct CMOS to LSTTL / TTL interfaces. Electrical parameters are similar to the JEDEC registered 4N49 optocoupler but with much better CTR degradation characteristics due to radiation exposure.

Device mounting is achieved by standard hybrid assembly with non-conductive epoxies. Gold or aluminum wire bonding can be used to make electrical connections for maximum placement flexibility.

Special electrical parametric selections are available on request.

#### NOTES:

<sup>1.</sup> Measured between pins 1 and 6 shorted together and pins 2,3,4,and 5 shorted together.  $T_A = 25^{\circ}C$  and duration = 1 second.

<sup>2.</sup> Derate linearly at 3.0 mW / °C above 25 °C

<sup>3.</sup> Value applies for Pw  $\leq$  1  $\mu S,$  PRR  $\leq$  300 pps.

<sup>4.</sup> Contact factory for more information

# Absolute Maximum Ratings

Coupled	
Input to Output Isolation Voltage <sup>1</sup> Storage Temperature Range Operation Temperature Range Mounting Temperature Range ( 3 minutes max. )	± 1500 Vdc -65°C to +150°C -55°C to +125°C 240°C
Input Diode	
Average Input Current Peak Forward Current (≤ 1mS duration ) Reverse Voltage Power Dissipation	40 mA 60 mA 3.0 V 70 mW
Output Detector	
Collector - Emitter Voltage Emitter - Collector Voltage Collector - Base Voltage	40 V 7 V 45 V
Power Dissipation	200 mW <sup>3</sup>

## **ELECTRICAL CHARACTERISTIC** (T<sub>A</sub> = 25 °C, Unless Otherwise Specified )

Parameter	Symbol	Min	Max	Units	Test Conditions	Fig.	Note
On-State Collector Current	IC (ON)	2.0 2.8 2.0	12	mA mA mA	I F = 1 mA, VCE = 5.0V I F = 2 mA, VCE = 5.0V, TA = -55°C I F = 2 mA, VCE = 5.0V, TA = 100°C	2,3	
On-State CollBase Current	ICB(ON)	30		μA	I		
Saturation Voltage	VCE(SAT)		0.3	V	I F = 2mA, IC =2.0mA		
Breakdown Voltage Collector to Emitter Collector to Base Emitter to Base Off-State Leakage Current Collector to Emitter Collector to Base Input Forward Voltage	BVCEO BVCBO BVEBO ICE(OFF) ICB(OFF) VF	40 45 7 1.8 1.4 1.2	100 100 10 2.2 1.8 1.6	V V ν nA μA nA V V V V	I CE = 1 mA I CB = 100 $\mu$ A I EB = 100 $\mu$ A VCE = 20V VCE = 20V, T <sub>A</sub> =100 °C VCB = 20V I F = 10mA, T <sub>A</sub> = -55°C I F = 10mA, T <sub>A</sub> = 100°C	1 1 1	
Input Reverse Current	IR		100	μA	V R = 2.0V		
Input to Output Resistance	<b>r</b> 1-0	10 11		Ω	V I-O = ±1000Vdc		1
Input to Output Capacitance	C I - O		5	pF	V I-0 = 0V, f = 1 MHz		1
Rise Time Fall Time	tr tf		25 25	μS μS	Vcc = 10V, RL = 100 Ω I F = 5mA	4	

#### 100 9 NORMALIZED COLLECTOR CURRENT 8 NORMALIZED TO: IF - FORWARD CURRENT (mA) **I** F = 1 mA 7 VCE = 5V 125 °C -55 °C 25 °C TA = 25 °C 10 6 5 4 3 2 1 .1 – 0.9 0 1.1 1.3 1.5 1.7 1.9 2.1 0 1 2 3 4 5 6 7 8 9 10 V F - FORWARD VOLTAGE (V) I F = FORWARD CURRENT ( mA ) Fig. 2 - Normalized Ic vs. I F Fig. 1 - Diode Forward Characteristics 1.8 NORMALIZED TO: 1.6 VCE = 5V TA = 25 °C 1.4 NORMALIZED CTRCE 1.2 1.0 0.8 ΙĒ = 1 mA 0.6 0.4 0.2 L -75 -50 -25 0 25 50 75 100 125 150 AMBIENT TEMPERATURE (°C) Fig. 3 - Normalized CTR vs. Temperature INPUT Vcc IF 0 Pulse Width = 100µS IF Duty Cycle = 1% 0 90 % Vout 10 % 0 Vout ο t r t f 100Ω R∟ ≶

### **TYPICAL PERFORMANCE CURVES**

Fig. 4 - Switching Test Circuit