## Analog Switch IC

## Description

The integrated circuit $\alpha 1510$ is used to switch arbitrary loads on AC lines.
These loads may be inductivities, capacities or resistances.
The circuit has a CMOS/TTL input with schmitt trigger character.
Integrated functions protect the circuit versus short circuit and overtemperature.

## Features

- Small switch-ON-resistance $<15 \Omega$
$\square$ Stand-by power dissipation < 0.02W
$\square$ Minimum external components
- ESD-protected input
- Integrated short circuit and overtemperature protection
- Temperature range $0^{\circ} \mathrm{C} \ldots+70^{\circ} \mathrm{C}$
- Package SOP12LP 1510BT


## Applications

- General switching element for analog voltages

Electronic switch for power supply engines, relays, magnetic valves, etc.

## Typical Application




## Pin Definition

| Pin | Symbol | Designation |
| :---: | :--- | :--- |
| 5 | SW1 | Switch port 1 |
| 1 | SW2 | Switch port 2 |
| 6 | SW3 | Switch port 3 |
| 7 | IN | Input |
| 8 | VDS | Power Supply |
| M | GND | Ground |

The location of pins can be changed during the development

## General function and description

The $\alpha 1510$ is an electronic switch for common applications on the $120 \mathrm{~V} / 230 \mathrm{~V}$ main and allows to switch inductive, capacitive and resistive loads. The switch disposes an CMOS / TTL input. The IC is protected versus short circuit and overtemperature.

Two fundamental configurations of the switch are possible:

- A bi-directional switch without a bridge rectifier is realisable by a series connection of the two integrated high voltage DMOS transistors.
- The parallel connection of the high voltage DMOS transistors allows an unidirectional application.


## PIN function and description

## VDS

The $\alpha 1510$ is designed for a supply voltage of VDS $=10 \ldots 14 \mathrm{~V}$. This pin supplies the control circuit with an internal oscillator.

## GND

The ground pin supplies the control circuit.

## IN

The input of the switch is designed for TTL and CMOS applications. This pin owns an internal pull up to support a simple input circuit.

## SW1

SW1 is the Drain of a high voltage DMOS - Transistor. The SW1 is dielectric isolated from the control circuit

## SW2

SW2 educates the common Source of 2 high voltage DMOS - Transistors. The SW2 is dielectric isolated from the control circuit

## SW3

SW3 is the Drain of a high voltage DMOS - Transistor. The SW3 is dielectric isolated from the control circuit

## Absolute Maximum Ratings

at $\mathrm{T}_{\mathrm{amb}}=0^{\circ} \mathrm{C} \ldots+70^{\circ} \mathrm{C}$

| Symbol | Parameter | Conditions | Min | Max | Unit |
| :---: | :--- | :--- | :---: | :---: | :---: |
| $\mathrm{V}_{\text {SW } 13}$ | Switching Voltage 1-3 |  |  | 276 | $\mathrm{~V}_{\text {rms }}$ |
| $\mathrm{V}_{\text {SW32 }}$ | Switching Voltage 1/3-2 | short between Pin5 and Pin6 |  | 400 | V |
| $\mathrm{~V}_{\text {DIE }}$ | Dielectric Voltage <br> (Switch 1/2/3-Ground M) | short between Pin1; <br> Pin5 and Pin6 |  | $\pm 500$ | V |
| $\mathrm{~V}_{\text {DS }}$ | Power Supply |  | -0.7 | 15 | V |
| $\mathrm{~V}_{\text {IN }}$ | Input Voltage |  | -0.7 | 7 | V |
| $\mathrm{P}_{\text {TOT }}$ | Power Dissipation |  |  | 1 | W |
| $\mathrm{~T}_{\text {amb }}$ | Operating temperature |  | 0 | 70 | ${ }^{\circ} \mathrm{C}$ |
| $\mathrm{T}_{\text {SD }}$ | Switch off temperature (Ther- <br> mal shutdown) |  | 150 | 170 | ${ }^{\circ} \mathrm{C}$ |
| $\mathrm{T}_{\mathrm{j}}$ | Junction temperature |  | -25 | 150 | ${ }^{\circ} \mathrm{C}$ |
| $\mathrm{T}_{\text {Stg }}$ | Storage temperature |  | -55 | 150 | ${ }^{\circ} \mathrm{C}$ |
| $\mathrm{R}_{\text {thia }}$ | Thermal resistance <br> (junction - ambient) |  |  | 70 | $\mathrm{~K} / \mathrm{W}$ |

## Electrical Characteristics

DC Characteristics
at $\mathrm{T}_{\mathrm{a}}=0^{\circ} \mathrm{C} \ldots 70^{\circ} \mathrm{C}$, unless otherwise specified

| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\mathrm{I}_{\text {sw(laek) }}$ | Leakage current of the switch | $\mathrm{V}_{\text {Sw } 13}=300 \mathrm{~V}$ |  |  | 10 | $\mu \mathrm{A}$ |
| $\mathrm{R}_{\text {SW } 13 \text { ON }}$ | On resistance of the switch | $\mathrm{l}_{\mathrm{sw} 13}=0.5 \mathrm{~A}$ |  | 45 | 50 | $\Omega$ |
| $\mathrm{V}_{\text {DS }}$ | Power Supply |  | 10 |  | 14 | V |
| $\mathrm{l}_{\text {DS }}$ | Current Supply | $\mathrm{V}_{\text {DS }}=12 \mathrm{~V}$ |  | 2 | 4 | mA |
| $\mathrm{V}_{\text {IN Low }}$ | Input voltage low |  | -0.3 |  | 0.8 | V |
| $\mathrm{V}_{\text {IN High }}$ | Input voltage high |  | 2 |  | 5 | V |
| - ${ }_{\text {IN Low }}$ | Input current low | $\mathrm{V}_{\mathrm{IN}}=0 \mathrm{~V}$ |  |  | 30 | $\mu \mathrm{A}$ |
| IN High | Input current high | $\mathrm{V}_{\mathrm{IN}}=5 \mathrm{~V}$ |  |  | 10 | $\mu \mathrm{A}$ |

## AC Characteristics

at $\mathrm{T}_{\mathrm{a}}=25^{\circ} \mathrm{C}$, unless otherwise specified

| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
| :---: | :--- | :--- | :---: | :---: | :---: | :---: |
| $\mathrm{t}_{1}$ | Turn on delay time of the switch | $\mathrm{V}_{\mathrm{sw} 13}=300 \mathrm{~V} ;$ <br> $\mathrm{I}_{\mathrm{sw} 13}=0.5 \mathrm{~A}$ |  |  | 10 | $\mu \mathrm{~s}$ |
| $\mathrm{t}_{2}$ | Turn on time of the switch | $\mathrm{V}_{\mathrm{sw} 13}=300 \mathrm{~V} ;$ <br> $\mathrm{I}_{\mathrm{sw} 13}=0.5 \mathrm{~A}$ |  |  | 20 | $\mu \mathrm{~s}$ |
| $\mathrm{t}_{3}$ | Turn off delay time of the switch | $\mathrm{V}_{\mathrm{sw} 13}=300 \mathrm{~V} ;$ <br> $\mathrm{I}_{\mathrm{sw} 13}=0.5 \mathrm{~A}$ |  |  | 10 | $\mu \mathrm{~s}$ |
| $\mathrm{t}_{4}$ | Turn off time of the switch | $\mathrm{V}_{\mathrm{sw} 13}=300 \mathrm{~V} ;$ <br> $\mathrm{I}_{\mathrm{sw} 13}=0.5 \mathrm{~A}$ |  |  | 20 | $\mu \mathrm{~s}$ |

## Further application

Examples for further applications of the $\alpha 1510$ are:

- Speed regulation of an engine (See Fig.1)
- Remote control of an incandescent lamp (See Fig.2)
- DC Load (See Fig.3)


Fig. 1 Speed regulation of an engine


Fig. 2 Remote control of an incandescent lamp


Fig. 3 DC Load


SOP 12LP

## Note

It is not given warranty that the declared circuits, devices, facilities; components, assembly groups or treatments included herein are free from legal claims of third parties.
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