

PRELIMINARY

Data Sheet June 20, 2005

FN7377.0

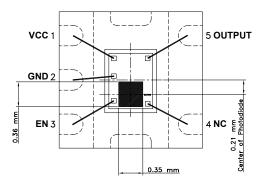
Ambient Light Photo Detect IC

The EL7900 is a light-to-current optical sensor combining a photodiode and a current amplifier on a single monolithic IC. Output current is directly proportionate to the light intensity on the photodiode. Its sensitivity is superior to that of a phototransistor and exhibits little variation. Its spectral sensitivity matches closely to the luminous efficiency and linearity.

Housed in an ultra-compact surface mount clear plastic package, this device is excellent for power saving control function in cell phones, PDAs, and other handheld applications.

Pinout

EL7900 (5-PIN DFN) TOP VIEW



Features

- · Monolithic IC containing photodiode and amplifier
- · Converts light intensity to current
- · 2.5V to 5.5V supply range
- · Low supply current 1µA
- · Excellent output linearity of luminance
- · Ultra-compact and light surface mount package
- · Pb-Free plus anneal available (RoHS compliant)

Applications

- · Mobile phones
- Notebook PCs
- PDAs
- · Video cameras
- · Digital cameras

Ordering Information

PART NUMBER	PACKAGE	TAPE & REEL	PKG. DWG. #	
EL7900ILCZ (See Note)	5-Pin ODFN (Pb-free)	-	MDP0052	

NOTE: Intersil Pb-free plus anneal products employ special Pb-free material sets; molding compounds/die attach materials and 100% matte tin plate termination finish, which are RoHS compliant and compatible with both SnPb and Pb-free soldering operations. Intersil Pb-free products are MSL classified at Pb-free peak reflow temperatures that meet or exceed the Pb-free requirements of IPC/JEDEC J STD-020.

Absolute Maximum Ratings (T_A = 25°C)

Supply Voltage between V _{SD} and GND 6V	Maximum Die Temperature
Maximum Continuous Output Current	Storage Temperature65°C to +150°C
Operating Temperature	

CAUTION: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

IMPORTANT NOTE: All parameters having Min/Max specifications are guaranteed. Typical values are for information purposes only. Unless otherwise noted, all tests are at the specified temperature and are pulsed tests, therefore: $T_J = T_C = T_A$

 $\textbf{Electrical Specifications} \hspace{0.5cm} V_{CC} = 3V, \, T_{A} = 25^{\circ}C, \, \text{fluorescent light, unless otherwise specified.}$

PARAMETER	DESCRIPTION	CONDITION	MIN	TYP	MAX	UNIT
I _{CC}	Supply Current	R _L = 1kΩ, EV = 100lx		62		μΑ
		EV = 0lx		1		μA
I _{L1}	Light Current	EV = 100lx	39	60.5	82	μA
I _{L2}	Light Current	EV = 10lx		6.2		μA
I _{LEAK}	Dark Current	EV = 0lx		0.15		μA
V _{O-MAX}	Maximum Output Compliance Voltage	at 95% of nominal output current, EV = 100lx		2.7		V
T _R	Rise Time (See Note)	R _L = 5kΩ, EV = 300lx		105	125	μs
T _F	Fall Time (See Note)	R _L = 5kΩ, EV = 300lx		170	225	μs
T _D	Delay Time for Rising Edge (See Note)	R _L = 5kΩ, EV = 300lx		165	200	μs
T _S	Delay Time for Falling Edge (See Note)	R _L = 5kΩ, EV = 300lx		65	85	μs
V _{LO}	Maximum Voltage at EN Pin to Enable				0.6	V
V _{HI}	Minimum Voltage at EN Pin to Disable		1.8			V

NOTE: Switching time measurement is based on Figures 1 and 2.

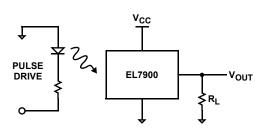


FIGURE 1. RISE/FALL TIME MEASUREMENT

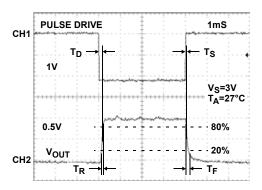


FIGURE 2.

Typical Performance Curves

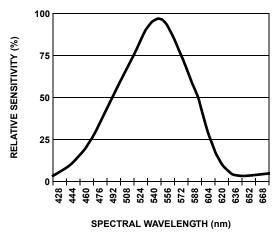


FIGURE 3. RELATIVE SENSITIVITY

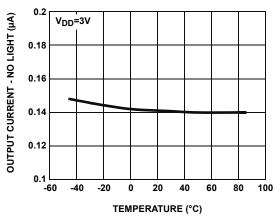


FIGURE 5. DARK CURRENT vs TEMPERATURE

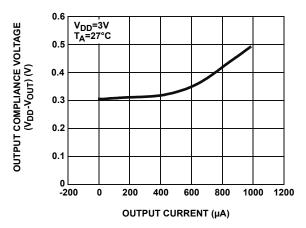


FIGURE 7. OUTPUT COMPLIANCE VOLTAGE vs CURRENT

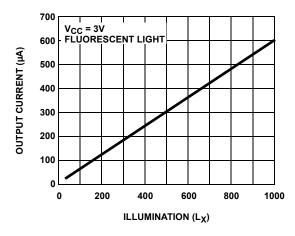


FIGURE 4. SENSITIVITY

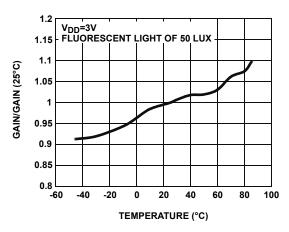
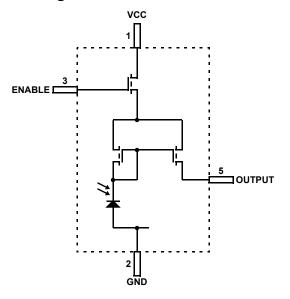


FIGURE 6. GAIN vs TEMPERATURE

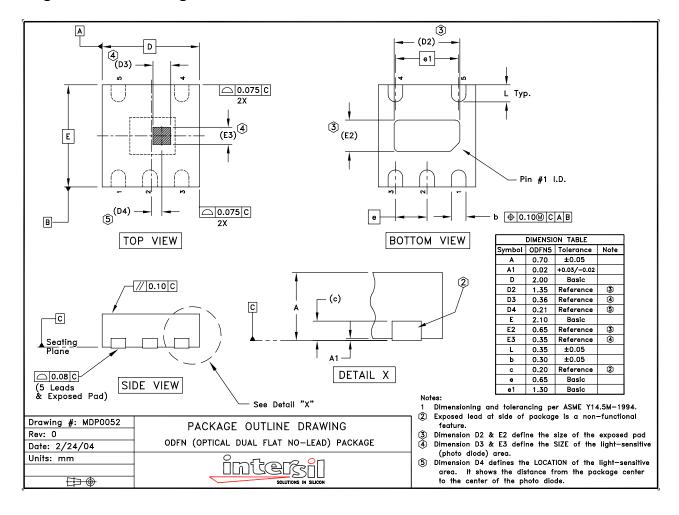
Pin Descriptions

PIN	NAME	DESCRIPTION
1	VCC	Supply, 2.5V to 5.5V
2	GND	Ground
3	EN	Enable
4	NC	No connect
5	Output	Current output pin

Block Diagram



Package Outline Drawing



NOTE: The package drawing shown here may not be the latest version. To check the latest revision, please refer to the Intersil website at http://www.intersil.com/design/packages/index.asp

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