

QUICKSWITCH[®] PRODUCTS *IDTQS32XR861* HIGH-SPEED CMOS 20-BIT LOW RESISTANCE, ACTIVE LOW BUS SWITCH WITH FLOW-THROUGH PINOUT

FEATURES:

- · Enhanced N channel FET with no inherent diode to Vcc
- 2.5Ω bidirectional switches connect inputs to outputs
- · Zero propagation delay, zero ground bounce
- · Undershoot clamp diodes on all switch and control inputs
- · Available in 48-pin QVSOP package

APPLICATIONS:

- Hot-swapping, hot-docking (low resistance for PCI and Compact PCI applications)
- Voltage translation (5V to 3.3V)
- Power conservation
- · Capacitance reduction and isolation
- Bus isolation
- Clock gating

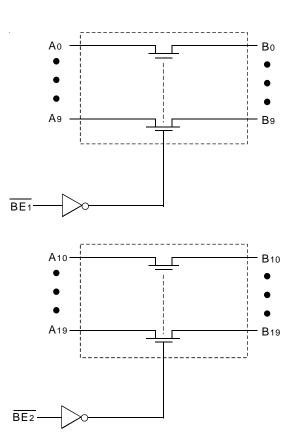
DESCRIPTION:

The QS32XR861 provides two sets of ten high-speed CMOS, TTLcompatible, low resistance bus switches. The very low ON resistance (2.5Ω) of the QS32XR861 allows inputs to be connected to outputs without adding propagation delay and without generating additional ground bounce noise. The switches are controlled by independent active Low Enable (\overline{BE}).

The QS32XR861 is ideal for switching digital buses, as well as for hot plug buffering and 5V to 3.3V conversion. The low ON resistance of the QS32XR861 makes it ideal for PCI hot-docking applications.

The QS32XR831 is characterized for operation at -40°C to +85°C.

FUNCTIONAL BLOCK DIAGRAM

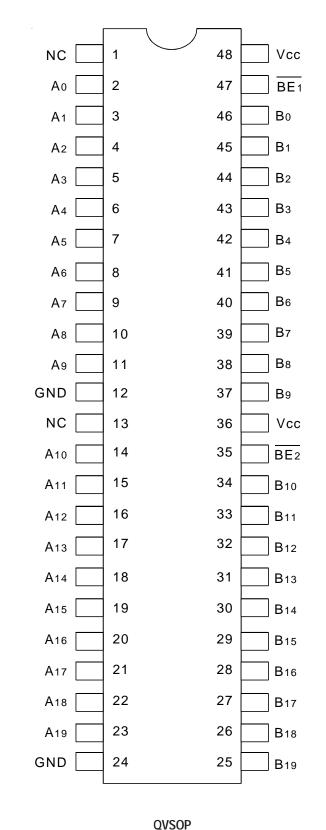


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PINCONFIGURATION



ABSOLUTE MAXIMUM RATINGS(1)

Symbol	Description	Max	Unit
VTERM ⁽²⁾	Supply Voltage to Ground	–0.5 to +7	V
VTERM ⁽³⁾	DC Switch Voltage Vs	–0.5 to +7	V
VTERM ⁽³⁾	DC Input Voltage VIN	–0.5 to +7	V
VAC	AC Input Voltage (pulse width \leq 20ns)	-3	V
Ιουτ	DC Output Current	120	mA
Рмах	Maximum Power Dissipation (TA = 85°C)	0.5	W
Tstg	Storage Temperature	-65 to +150	°C

NOTE:

 Stresses greater than those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

2. Vcc terminals.

3. All terminals except Vcc.

CAPACITANCE

 $(TA = +25^{\circ}C, f = 1.0MHz, VIN = 0V, VOUT = 0V)$

Pins	Тур.	Max. ⁽¹⁾	Unit
Control Pins	3	4	pF
Quickswitch Channels (Switch OFF)	5	6	pF

NOTE:

1. This parameter is measured at characterization but not tested.

PINDESCRIPTION

Pin Names	I/O	Description		
A0 - A19	I/O	Bus A		
B0 - B19	I/O	Bus B		
BEn	Ι	Bus Enable		

FUNCTION TABLE⁽¹⁾

BE1	BE2	A0 - A9	A10 - A19	Function
L	L	Bo - B9	B10 - B19	Connect
L	Н	B0 - B9	Z	Connect
Н	L	Z	B10 - B19	Connect
Н	Н	Z	Z	Disconnect

NOTE:

1. H = HIGH Voltage Level

L = LOW Voltage Level

Z = High-Impedance

TOP VIEW

DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE

 ${\sf Following\,Conditions\,Apply\,Unless\,Otherwise\,Specified:}$

Industrial: TA = -40° C to $+85^{\circ}$ C, VCC = $5.0V \pm 10\%$

Symbol	Parameter	Test Conditions	Min.	Typ. ⁽¹⁾	Max.	Unit
Vih	Input HIGH Level	Guaranteed Logic HIGH for Control Pins	2	_	_	V
Vil	Input LOW Level	Guaranteed Logic LOW for Control Pins	_	—	0.8	V
lin	Input LeakageCurrent (Control Inputs)	$0V \le VIN \le VCC$	_	0.01	±1	μA
loz	Off-State Output Current (Hi-Z)	$0V \le VOUT \le Vcc$, Switches OFF	_	0.01	±1	μA
Ron	Switch ON Resistance ⁽²⁾	VCC = Min., VIN = 0V, ION = 30mA	_	2.5	5	Ω
		VCC = Min., VIN = 2.4V, ION =15mA	—	4	8.5	
Vp	Pass Voltage ⁽³⁾	Vcc = 5V, Iout = -5µA	3.7	4	4.3	V

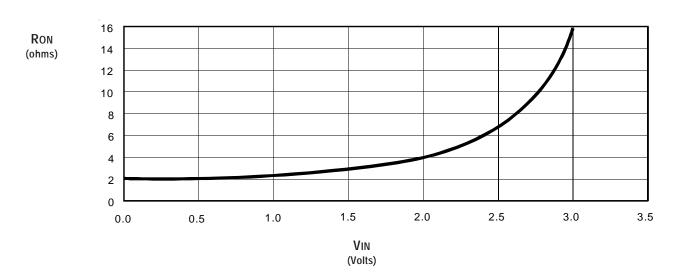
NOTES:

1. Typical values are at Vcc = 5.0V, TA = 25°C.

2. Max. value of Ron is guaranteed but not production tested.

3. Pass Voltage is guaranteed but not production tested.

TYPICAL ON RESISTANCE vs VIN AT Vcc = 5V



POWER SUPPLY CHARACTERISTICS

Symbol	Parameter	Test Conditions ⁽¹⁾	Тур ⁽²⁾	Max.	Unit
Icco	Quiescent Power Supply Current	Vcc = Max., \overline{BEn} = GND or Vcc, f = 0	0.2	6	μA
Δlcc	Power Supply Current per Control Input HIGH ⁽³⁾	Vcc = Max., VIN = 3.4V, f = 0	_	2.5	mA
ICCD	Dynamic Power Supply Current per MHz ⁽⁴⁾	Vcc = Max., A and B pins open	—	0.25	mA/MHz
		Control Input Toggling at 50% Duty Cycle			

NOTES:

1. For conditions shown as Min. or Max., use the appropriate values specified under DC Electrical Characteristics.

2. Typical values are at Vcc = 5.0V, 25°C ambient.

3. Per TLL driven input (VIN = 3.4V, control inputs only). A and B pins do not contribute to ∆Icc.

4. This current applies to the control inputs only and represents the current required to switch internal capacitance at the specified frequency. The A and B pins generate no significant AC or DC currents as they transition. This parameter is guaranteed but not production tested.

SWITCHING CHARACTERISTICS OVER OPERATING RANGE

 $T_A = -40^{\circ}C \text{ to } +85^{\circ}C$, $V_{CC} = 5.0V \pm 10\%$;

CLOAD = 50pF, $RLOAD = 500\Omega$ unless otherwise noted.

Symbol	Parameter	Min. ⁽¹⁾	Тур.	Max.	Unit
t PLH	Data Propagation Delay ^(2,3)	—	—	0.12	ns
t PHL	An to/from Bn				
tрzн	Switch Turn-on Delay	1.5	—	5.6	ns
tPZL	BEn to An/Bn				
tphz	Switch Turn-off Delay ⁽²⁾	1.5	—	4.5	ns
tPLZ	BEn to An/Bn				

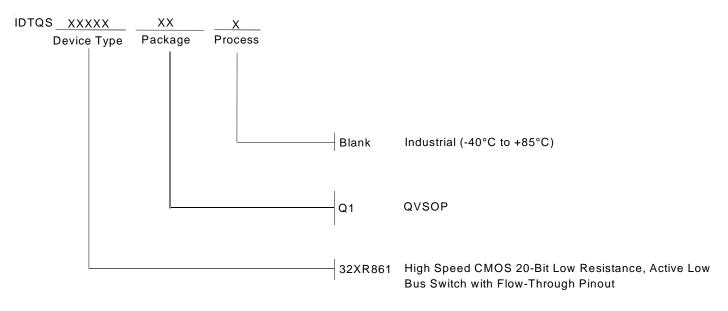
NOTES:

1. Minimums are guaranteed but not production tested.

2. This parameter is guaranteed but not production tested.

^{3.} The bus switch contributes no propagation delay other than the RC delay of the ON resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.12ns for CL = 50pF. Since this time constant is much smaller than the rise and fall times of typical driving signals, it adds very little propagation delay to the system. Propagation delay of the bus switch, when used in a system, is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

ORDERING INFORMATION





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