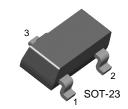


## **MMBT3904K**

## **NPN Epitaxial Silicon Transistor**

## **General Purpose Transistor**





1. Base 2. Emitter 3. Collector

## Absolute Maximum Ratings T<sub>a</sub> = 25°C unless otherwise noted

Symbol	Parameter	Value	Units
V <sub>CBO</sub>	Collector-Base Voltage	60	V
V <sub>CEO</sub>	Collector-Emitter Voltage	40	V
V <sub>EBO</sub>	Emitter-Base Voltage	6	V
I <sub>C</sub>	Collector Current	200	mA
P <sub>C</sub>	Collector Power Dissipation	350	mW
T <sub>STG</sub>	Storage Temperature	150	°C

## $\textbf{Electrical Characteristics} \ \, \textbf{T}_{a} \!\!=\!\! 25^{\circ} \text{C unless otherwise noted}$

Symbol	Parameter	Test Condition	Min.	Max.	Units
BV <sub>CBO</sub>	Collector-Base Breakdown Voltage	$I_C = 10\mu A, I_E = 0$	60		V
BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage *	$I_C = 1 \text{mA}, I_B = 0$	40		V
BV <sub>EBO</sub>	Emitter-Base Breakdown Voltage	$I_E = 10\mu A, I_C = 0$	6		V
I <sub>CEX</sub>	Collector Cut-off Current	V <sub>CE</sub> = 30V, V <sub>EB</sub> = 3V		50	nA
h <sub>FE</sub>	DC Current Gain *	$V_{CE} = 1V, I_{C} = 0.1 \text{mA}$ $V_{CE} = 1V, I_{C} = 1 \text{mA}$ $V_{CE} = 1V, I_{C} = 10 \text{mA}$ $V_{CE} = 1V, I_{C} = 50 \text{mA}$ $V_{CE} = 1V, I_{C} = 100 \text{mA}$	40 70 100 60 30	300	
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage *	$I_C = 10$ mA, $I_B = 1$ mA $I_C = 50$ mA, $I_B = 5$ mA		0.2 0.3	V V
V <sub>BE</sub> (sat)	Base-Emitter Saturation Voltage *	$I_C = 10$ mA, $I_B = 1$ mA $I_C = 50$ mA, $I_B = 5$ mA	0.65	0.85 0.95	V V
C <sub>ob</sub>	Output Capacitance	V <sub>CB</sub> = 5V, I <sub>E</sub> = 0, f = 1MHz		4	pF
f <sub>T</sub>	Current Gain-Bandwidth Product	V <sub>CE</sub> = 20V, I <sub>C</sub> = 10mA, f = 100MHz	300		MHz
NF	Noise Figure	$I_C = 100\mu A, V_{CE} = 5V, R_S = 1K\Omega$ f = 10Hz to 15.7KHz		5	dB
t <sub>ON</sub>	Turn On Time	$V_{CC} = 3V, V_{BE} = 0.5V$ $I_{C} = 10mA, I_{B1} = 1mA$		70	ns
t <sub>OFF</sub>	Turn Off Time	$V_{CC} = 3V$ , $I_C = 10mA$ , $I_{B1} = I_{B2} = 1mA$		250	ns

<sup>\*</sup> Pulse Test: Pulse Width≤300μs, Duty Cycle≤2%

## **Typical Performance Characteristics**

Figure 1. DC current Gain

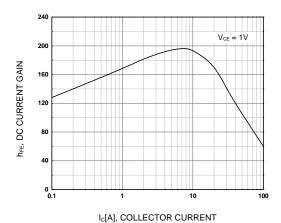


Figure 3. Output Capacitance

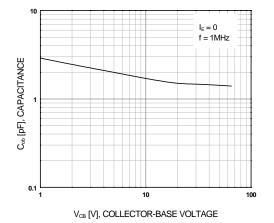


Figure 2. Base-Emitter Saturation Voltage Collector-Emitter Saturation Voltage

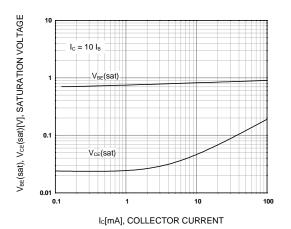
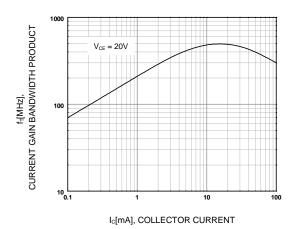


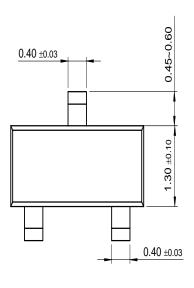
Figure 4. Current Gain Bandwidth Product

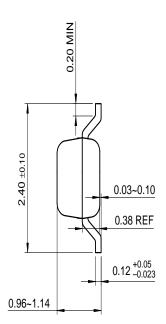


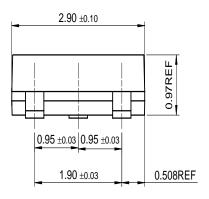
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## **Mechanical Dimensions**

# SOT-23







Dimensions in Millimeters

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