Ordering number : ENN6938

N-Channel Silicon MOSFET

FSS244

DC / DC Converter Applications

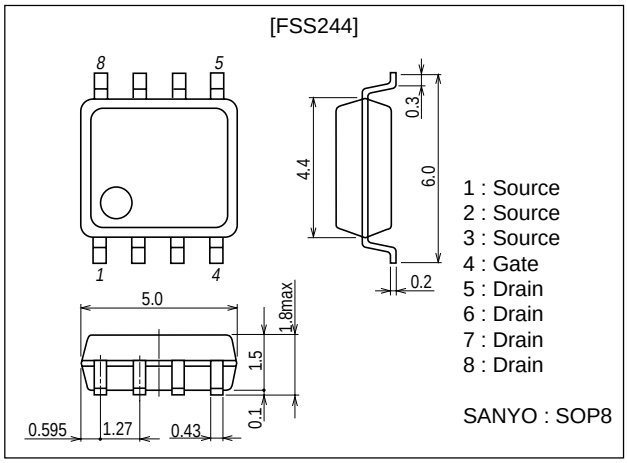
Features

- Low ON-resistance.
- 4V drive.
- Ultrahigh speed switching.

Package Dimensions

unit : mm

2116



Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V_{DSS}		30	V
Gate-to-Source Voltage	V_{GSS}		±20	V
Drain Current (DC)	I_D		10	A
Drain Current (Pulse)	I_{DP}	PW≤10μs, duty cycle≤1%	52	A
Allowable Power Dissipation	P_D	Mounted on a ceramic board (1200mm²×0.8mm) 1unit	2.0	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	I_D=1mA, V_{GS}=0	30			V
Zero-Gate Voltage Drain Current	I_{DSS}	V_{DS}=30V, V_{GS}=0			1	μA
Gate-to-Source Leakage Current	I_{GSS}	V_{GS}=±16V, V_{DS}=0			±10	μA
Cutoff Voltage	V_{GS}(off)	V_{DS}=10V, I_D=1mA	1.0		2.4	V
Forward Transfer Admittance	|yfs|	V_{DS}=10V, I_D=10A	12	18		S

Marking : S244

Continued on next page.

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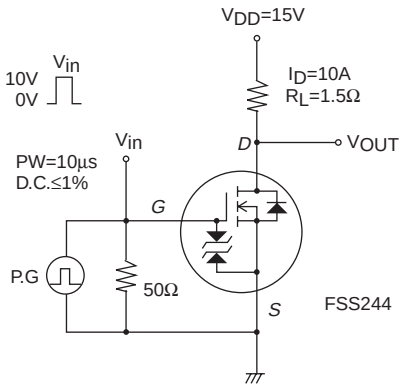
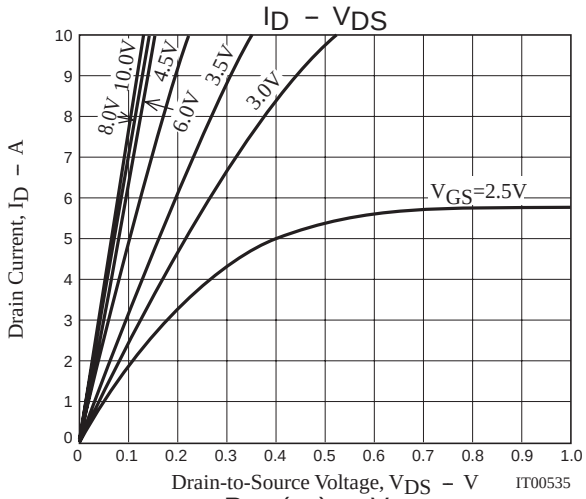
TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110-8534 JAPAN

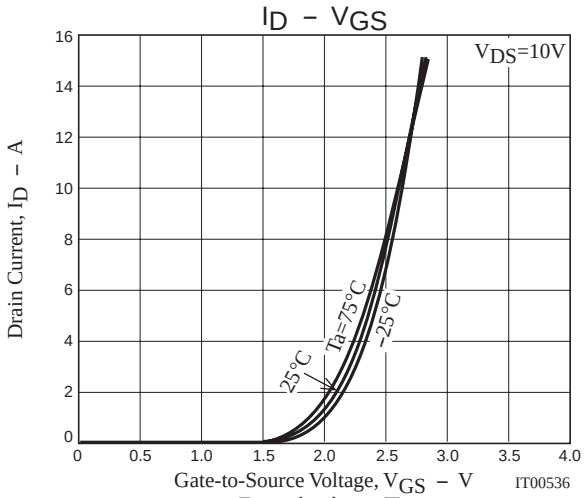
Continued from preceding page.

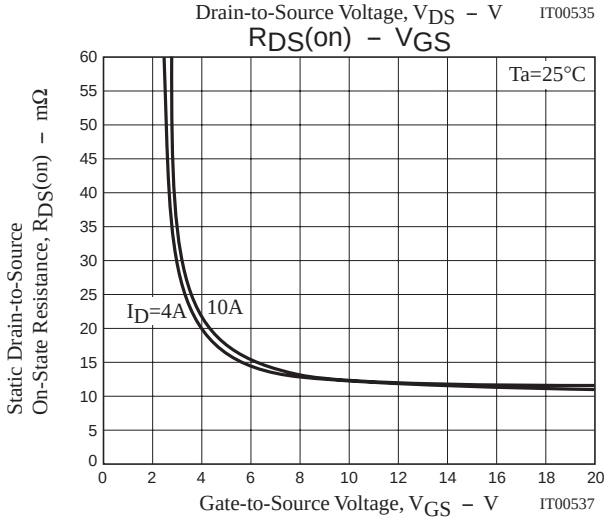
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Static Drain-to-Source On-State Resistance	$R_{DS}(on)1$	I_D=10A, V_{GS}=10V		13	17	mΩ
	$R_{DS}(on)2$	I_D=4A, V_{GS}=4.5V		20	28	mΩ
Input Capacitance	Ciss	V_{DS}=10V, f=1MHz		980		pF
Output Capacitance	Coss	V_{DS}=10V, f=1MHz		410		pF
Reverse Transfer Capacitance	Crss	V_{DS}=10V, f=1MHz		170		pF
Turn-ON Delay Time	$t_d(on)$	See specified Test Circuit		11		ns
Rise Time	t_r	See specified Test Circuit		210		ns
Turn-OFF Delay Time	$t_d(off)$	See specified Test Circuit		80		ns
Fall Time	t_f	See specified Test Circuit		85		ns
Total Gate Charge	Qg	V_{DS}=10V, V_{GS}=10V, I_D=10A		17		nC
Gate-to-Source Charge	Qgs	V_{DS}=10V, V_{GS}=10V, I_D=10A		3.3		nC
Gate-to-Drain “Miller” Charge	Qgd	V_{DS}=10V, V_{GS}=10V, I_D=10A		1.7		nC
Diode Forward Voltage	V_{SD}	I_S=10A, V_{GS}=0		0.8	1.2	V

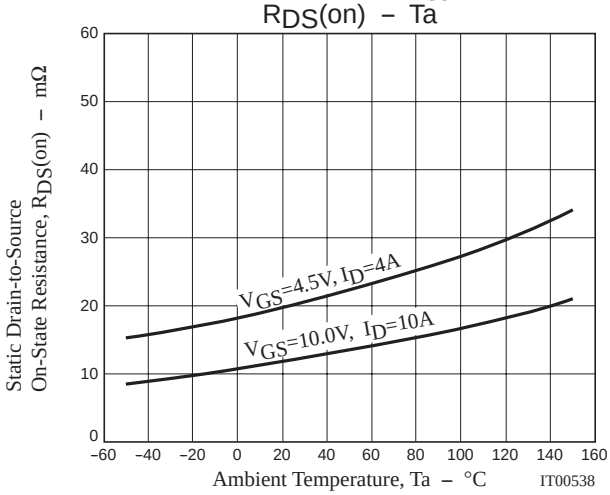
Switching Time Test Circuit

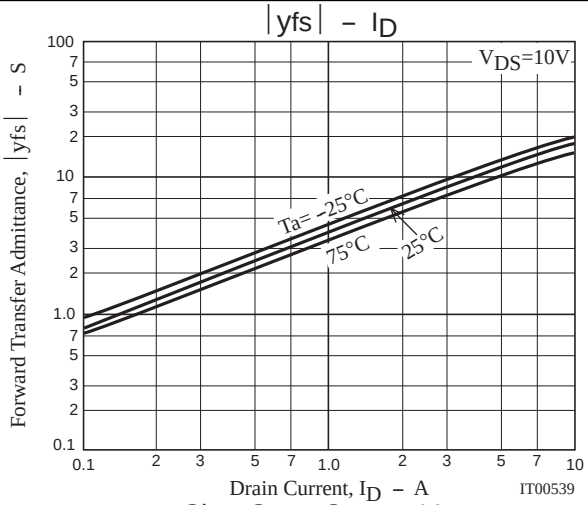


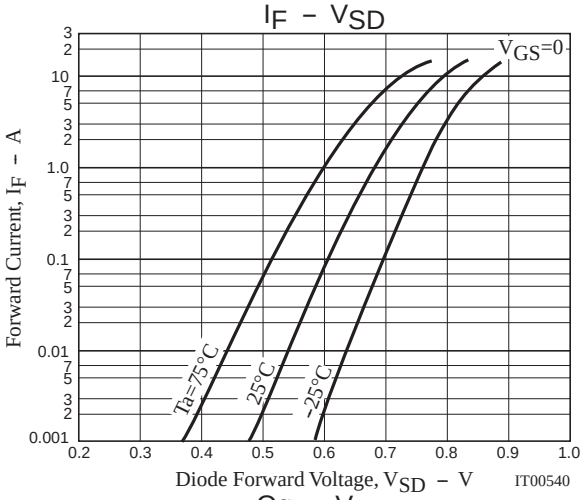


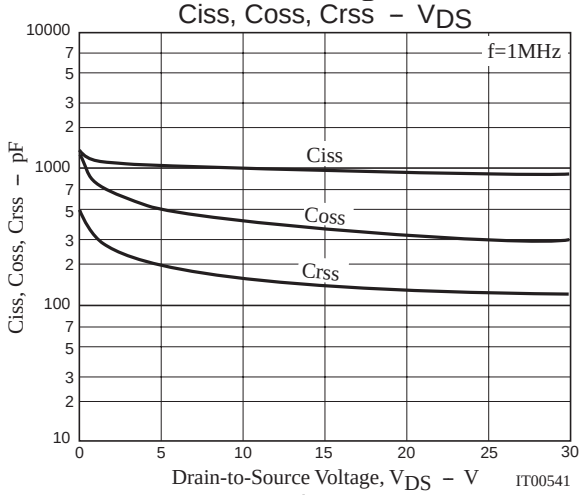


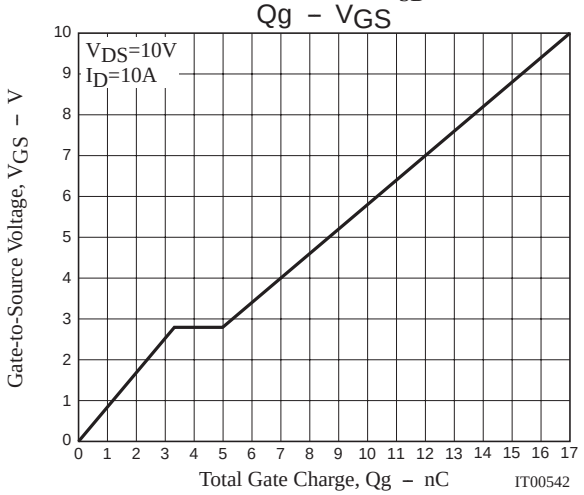


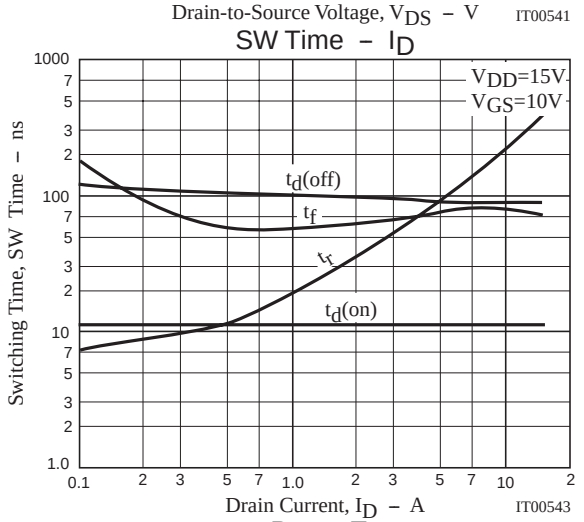


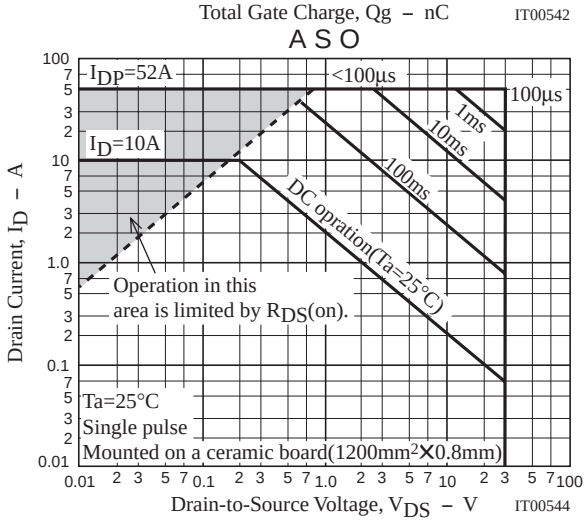

|yfs| – ID
VDS=10V
Ta=–25°C
75°C
25°C
Forward Transfer Admittance, |yfs| – S
Drain Current, ID – A
IT00539

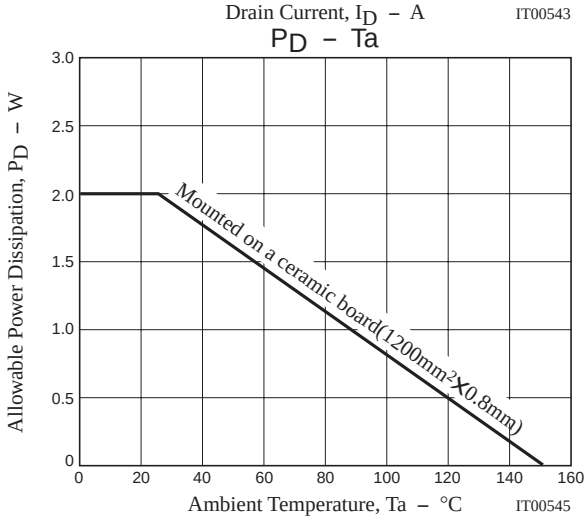

IF – VSD
VGS=0
Ta=75°C
25°C
–25°C
Forward Current, IF – A
Diode Forward Voltage, VSD – V
IT00540


Ciss, Coss, Crss – VDS
f=1MHz
Ciss
Coss
Crss
Ciss, Coss, Crss – pF
Drain-to-Source Voltage, VDS – V
IT00541


Qg – VGS
VDS=10V
ID=10A
Gate-to-Source Voltage, VGS – V
Total Gate Charge, Qg – nC
IT00542


SW Time – ID
VDD=15V
VGS=10V
td(off)
tf
tr
td(on)
Switching Time, SW Time – ns
Drain Current, ID – A
IT00543


A S O
IDP=52A
<100µs
100µs
1ms
10ms
100ms
DC opration(Ta=25°C)
ID=10A
Operation in this
area is limited by RDS(on).
Ta=25°C
Single pulse
Mounted on a ceramic board(1200mm²×0.8mm)
Drain Current, ID – A
Drain-to-Source Voltage, VDS – V
IT00544


PD – Ta
Mounted on a ceramic board(1200mm²×0.8mm)
Allowable Power Dissipation, PD – W
Ambient Temperature, Ta – °C
IT00545

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